



# **PRESIDENCY UNIVERSITY**

(Private University Estd. in Karnataka State by Act No. 41 of 2013)

## **PU-SoE-EEE 2021-22**

Ref. No. PU/ SoE/ EEE /2021-22/VAC/CIR/01

20-12-2021

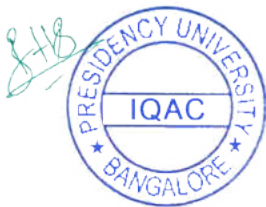
### **CIRCULAR**

#### **Sub: VALUE ADDED COURSES – OFFERED BY THE DEPT. OF EEE**

This is to inform all the students of the 3rd, 5th, and 7th semesters of B. Tech (EEE), the following value-added courses will be offered by the department during the AY 2021-22 (Fall Semester):

Sl. No	Course Code	Course Name	Name of the Faculty
1.	EEEV005	Auto Cad for Electrical Engineers	Mr. Ravi V Angadi
2.	EEEV011	Fundamentals of Scilab Programming	Dr Joshi Manohar
3.	EEEV012	Synchronized Phasor measurement in Grid using PMUs	Mr Bishakh Paul
4.	EEEV013	Simulation of Power Electronics circuits using MATLAB Simulink, Python and LTSpice	Mr. K Sreekanth Reddy
5.	EEEV017	Design of fuzzy Logic Systems	Mr Sarin M V
6.	EEEV018	Introduction to MATLAB/Simulink	Dr Jisha L K

All are informed to contact the respective course ICs of VAC based on your choice. The duration of the course is 30 hours. All the students are encouraged to attend VAC as per the course instructor's schedule. A certificate will be awarded after successful completion of the course.



  
Dr. Joshi Manohar  
HOD - EEE



**City Office: University House, 8/1, King Street, Richmond Town, Bengaluru - 560025**

**Campus: Presidency University, Itgalpura, Rajanukunte, Bengaluru - 560064**

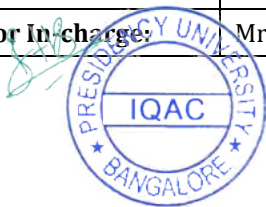
**Phone: + 80 4925 5533 / 5599 Email ID: [info@presidencyuniversity.in](mailto:info@presidencyuniversity.in)**

[www.presidencyuniversity.in](http://www.presidencyuniversity.in)



**School of Engineering**  
**Department of Electrical & Electronics Engineering**  
**Value Added Course offered during the Odd Semester 2021-22**

<b>Course Code:</b>	EEEV005
<b>Course Name:</b>	Auto CAD for Electrical Engineers
<b>Area of Specialization:</b>	Electrcal Engineering
<b>Course Description:</b>	This course contains a detailed explanation of AutoCAD Electrical tools and features. Every tool and feature is thoroughly explained with the help of examples. After going through this course, you will be able to create professional electrical control drawings with ease such as ladder diagrams, schematic drawings, panel drawings, parametric and nonparametric PLC modules, point-to-point wiring diagrams, report generation, creation of symbols, Circuit Builder, Terminal symbols, and so on.
<b>Course Outcome:</b>	On successful completion of the course, the student shall be able to: CO.1. Explain all AutoCAD Electrical tools and features CO.2. Develop professional electrical control drawings with ease. CO.3. Create a Panel Drawings, Wiring Diagram and creation of symbol. CO.4. Explain the various types of wire selection and PLC selection in CAD.
<b>Course Content:</b>	<b>Module No 1:</b> Basics Of Electrical Drawings: Introduction, Need of Drawings, Electrical Drawings, Common Symbols in Electrical Drawings, Wire and its Types, Labeling. [5- Hours] <b>Module No 2:</b> Introduction to AutoCAD Electrical and Interface: Introduction, System Requirement, Starting AutoCAD Electrical/AutoCAD, Creating A New Drawing Document, Meaning of Default templates, Electrical Templates, Application Menu. Starting Drawing, Open Options, Opening Drawing File Save, Applying Password on File, Save As, Export, Publish, Print Drawing Tab Bar, Drawing Area, Command Window, Bottom Bar, Drafting Settings dialog box [8- Hours] <b>Module No 3:</b> Project Management: Introduction, Project Management, Workflow in AutoCAD Electrical, Starting a New Project, Changing Properties of a project, Adding drawings in the project, Retagging and renumbering ladders in the drawings of project, Plotting/publishing project files, INSERTING COMPONENTS: Inserting Components using Icon menu, Inserting Components using Catalog Browser, Inserting Components using User Defined list, Inserting Components using Equipment list, Inserting Components using Panel list, Inserting Components using Terminal (Panel list), Pneumatic, Hydraulic, and P&ID components [9- Hours] <b>Module No 4:</b> Wires, Circuits, and Ladders: Inserting Wires, Applying wire numbers, Inserting user defined circuits, Inserting ladders, Cable Markers, Circuit Builders. Plcs and Components: Introduction, Application of PLCs in manufacturing process, Inserting Parametric PLCs, Inserting PLCs (Full Unit), Inserting Connectors, Inserting Terminals. [8- Hours]
<b>Instructor In-charge:</b>	Mr. Ravi V Angadi





## School of Engineering Department of Electrical & Electronics Engineering

AY 2021-22 (Odd Sem)

Value added Course(VAC) Name and Code: Auto CAD for Electrical Engineers & EEEV005

Name of the Instructor: Mr. Ravi V Angadi

### Attendance Sheet

S.No.	STUDENT ID NO	STUDENT NAME	Total classes conducted	Total classes attended	Percentage attended
1	20181EEE0001	ABHINAV SURESH	30	0	0.00
2	20181EEE0004	AMULYA A PUROHIT	30	24	80.00
3	20181EEE0006	ARSHAD SALEEM	30	22	73.33
4	20181EEE0007	ARVINDGOWDA C N	30	25	83.33
5	20181EEE0008	B H LAVANYA	30	22	73.33
6	20181EEE0019	HANUMANTH KUMAR A	30	30	100.00
7	20181EEE0022	JAYASHREE SAHU	30	30	100.00
8	20181EEE0025	K SHRAVAN KUMAR	30	30	100.00
9	20181EEE0034	MOHAMMED NASIR	30	22	73.33
10	20181EEE0038	PALLAVI R	30	27	90.00
11	20181EEE0039	PANDLA GURU SAI GOUD	30	3	10.00
12	20181EEE0050	SADIYA TAHERA	30	0	0.00
13	20181EEE0056	SHARON PRANATHI M	30	30	100.00
14	20181EEE0073	DILIP YADAV N	30	0	0.00
15	20191LEE0002	SHIVACHANDAN	30	6	20.00
16	20191LEE0006	DHEERAJ C	30	24	80.00
17	20191EEE0003	ANUSHA M JOLAD	30	23	76.67
18	20191EEE0011	KEERTHANA B R	30	0	0.00
19	20191EEE0013	KOTHAKOTA JAI RAMAKRISHNA	30	0	0.00
20	20191EEE0024	NAVYA SHREE M	30	18	60.00
21	20191EEE0028	PRAJWAL HOSAMANI	30	0	0.00
22	20191EEE0032	R S SHARUKH	30	30	100.00
23	20191EEE0033	ROSHAN S	30	0	0.00
24	20191EEE0037	SANJAY B	30	22	73.33
25	20191EEE0038	SANJAY P	30	0	0.00
26	20191EEE0040	SAPNA N	30	0	0.00



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27	20191EEE9002	KIRANMANOJ	30	0	0.00
28	20191EEE9003	SRINIVAS K	30	28	93.33
29	20201EEE0005	RAKSHITHA B	30	28	93.33
30	20201EEE0011	SAI NAYANA	30	21	70.00
31	20201EEE0015	ABHISHEK TT	30	30	100.00
32	20201EEE0022	YASHASWINI B G	30	22	73.33
33	20211LEE0004	YOGENDRA	30	0	0.00
34	20211LEE0009	PAVAN V	30	25	83.33
35	20211LEE0010	ROHIT GURUNATH MATHAPATI	30	22	73.33
36	20211LEE0011	KISHORE TEJA S N	30	24	80.00
37	20211LEE0013	CHARANREDDY S V	30	24	80.00
38	20211LEE0014	AMBIKA M BIJAPUR	30	30	100.00
39	20211LEE0016	NIRANJAN JAGADISH PAMMAR	30	30	100.00
40	20211LEE0017	NARESH R N	30	27	90.00
41	20211LEE0018	MURULI A V	30	30	100.00
42	20211LEE0021	CHARAN P	30	30	100.00
43	20211LEE0024	MAHESH M R	30	27	90.00
44	20211LEE0025	DARSHAN T C	30	24	80.00
45	20211LEE0027	KUSHAL R	30	30	100.00
46	20201LEE0009	BHARATH S	30	30	100.00
47	20191EEE9006	MOHAMMED ZUHAIB	30	27	90.00
48	20191LEE0001	NIHARIKA H	30	27	90.00
49	20211LEE0031	RAMEGOWDA K T	30	30	100.00
50	20211LEE0002	THATHIREDDY PERUMAL	30	23	76.67
51	20211LEE0029	ABHI J T	30	23	76.67
52	20211LEE0023	PATEL CHIKKALINGE GOWDA	30	23	76.67
Signature of Course Instructor					





## School of Engineering Department of Electrical & Electronics Engineering Value Added Course Marksheet

Course Code :	EEEEV005		Academic Year :			2021-22	
Course Name :	AutoCAD for Electrical Engineers		Semester :			Odd Semester	
			Instructor-in-Charge Name:			Mr. Ravi V Angadi	
			Instructor-in-Charge Employee ID :			PUNIV01021	
S. No	Roll No	Name	School SoE/SoL etc)	Attendance (in %)	Marks	Eligible for Certificate (Y/N)	Remark
1	20181EEE0001	ABHINAV SURESH	SoE	0.00	Ab	No	Not Eligible for Certificate
2	20181EEE0004	AMULYA A PUROHIT	SoE	80.00	50	Yes	
3	20181EEE0006	ARSHAD SALEEM	SoE	73.33	77.5	Yes	
4	20181EEE0007	ARVINDGOWDA C N	SoE	83.33	55	Yes	
5	20181EEE0008	B H LAVANYA	SoE	73.33	65	Yes	
6	20181EEE0019	HANUMANTH KUMAR A	SoE	100.00	75	Yes	
7	20181EEE0022	JAYASHREE SAHU	SoE	100.00	82.5	Yes	
8	20181EEE0025	K SHRAVAN KUMAR	SoE	100.00	77.5	Yes	
9	20181EEE0034	MOHAMMED NASIR	SoE	73.33	52.5	Yes	
10	20181EEE0038	PALLAVI R	SoE	90.00	57.5	Yes	
11	20181EEE0039	PANDLA GURU SAI GOUD	SoE	10.00	Ab	No	Not Eligible for Certificate
12	20181EEE0050	SADIYA TAHERA	SoE	0.00	Ab	No	Not Eligible for Certificate
13	20181EEE0056	SHARON PRANATHI M	SoE	100.00	90	Yes	
14	20181EEE0073	DILIP YADAV N	SoE	0.00	Ab	No	Not Eligible for Certificate
15	20191LEE0002	SHIVACHANDAN	SoE	20.00	Ab	No	Not Eligible for Certificate
16	20191LEE0006	DHEERAJ C	SoE	80.00	60	Yes	
17	20191EEE0003	ANUSHA M JOLAD	SoE	76.67	53	Yes	
18	20191EEE0011	KEERTHANA B R	SoE	0.00	42.5	No	Not Eligible for Certificate
19	20191EEE0013	KOTHAKOTA JAI RAMAKRISHNA	SoE	0.00	Ab	No	Not Eligible for Certificate
20	20191EEE0024	NAVYA SHREE M	SoE	60.00	Ab	No	Not Eligible for Certificate
21	20191EEE0028	PRAJWAL HOSAMANI	SoE	0.00	Ab	No	Not Eligible for Certificate
22	20191EEE0032	R S SHARUKH	SoE	100.00	56	Yes	
23	20191EEE0033	ROSHAN S	SoE	0.00	Ab	No	Not Eligible for Certificate



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24	20191EEE0037	SANJAY B	SoE	73.33	72.5	Yes	
25	20191EEE0038	SANJAY P	SoE	0.00	Ab	No	Not Eligible for Certificate
26	20191EEE0040	SAPNA N	SoE	0.00	50	Yes	
27	20191EEE9002	KIRANMANOJ	SoE	0.00	Ab	No	Not Eligible for Certificate
28	20191EEE9003	SRINIVAS K	SoE	93.33	57	Yes	
29	20201EEE0005	RAKSHITHA B	SoE	93.33	40	Yes	
30	20201EEE0011	SAI NAYANA	SoE	70.00	72.5	Yes	
31	20201EEE0015	ABHISHEK TT	SoE	100.00	65	Yes	
32	20201EEE0022	YASHASWINI B G	SoE	73.33	62.5	Yes	
33	20211LEE0004	YOGENDRA	SoE	0.00	Ab	No	Not Eligible for Certificate
34	20211LEE0009	PAVAN V	SoE	83.33	90	Yes	
35	20211LEE0010	ROHIT GURUNATH MATHAPATI	SoE	73.33	60	Yes	
36	20211LEE0011	KISHORE TEJA S N	SoE	80.00	42.5	Yes	
37	20211LEE0013	CHARANREDDY S V	SoE	80.00	55	Yes	
38	20211LEE0014	AMBIKA M BIJAPUR	SoE	100.00	72.5	Yes	
39	20211LEE0016	NIRANJAN JAGADISH PAMMAR	SoE	100.00	65	Yes	
40	20211LEE0017	NARESH R N	SoE	90.00	48	Yes	
41	20211LEE0018	MURULI A V	SoE	100.00	72.5	Yes	
42	20211LEE0021	CHARAN P	SoE	100.00	75	Yes	
43	20211LEE0024	MAHESH M R	SoE	90.00	62.5	Yes	
44	20211LEE0025	DARSHAN T C	SoE	80.00	45	Yes	
45	20211LEE0027	KUSHAL R	SoE	100.00	62.5	Yes	
46	20201LEE0009	BHARATH S	SoE	100.00	40	Yes	
47	20191EEE9006	MOHAMMED ZUHAIB	SoE	90.00	50	Yes	
48	20191LEE0001	NIHARIKA H	SoE	90.00	67.5	Yes	
49	20211LEE0031	RAMEGOWDA K T	SoE	100.00	47.5	Yes	
50	20211LEE0002	THATHIREDDY PERUMAL	SoE	76.67	60	Yes	
51	20211LEE0029	ABHI J T	SoE	76.67	57.5	Yes	
52	20211LEE0023	PATEL CHIKKALINGE GOWDA	SoE	76.67	55	Yes	

Name of Course

Mr. Ravi V Angadi

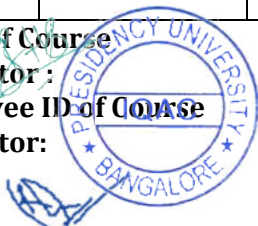
Instructor :

Employee ID of Course

PUNIV01021

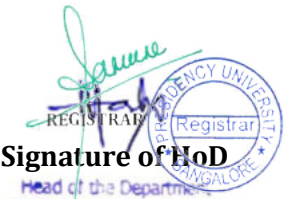
Instructor:

Signature of Instructor-in-charge



Signature of HoD

Head of the Department  
Electrical and Electronics Engineering  
School of Engineering  
PRESIDENCY UNIVERSITY  
Kajuraha, Yalahanka, Bengaluru - 56





**School of Engineering**  
**Department of Electrical & Electronics Engineering**  
**Value Added Course offered during the Odd Semester 2021-2022**

<b>Course Code:</b>	EEEV011
<b>Course Name:</b>	Fundamentals of Scilab programming
<b>Area of Specialization:</b>	Electrical Engineering
<b>Course Description:</b>	This course will introduce the basic concepts of scientific programming and simulations using Scilab. It will enable to perform numerical computations and analysis in all major scientific areas in all branches of engineering. This course requires the fundamentals of basic mathematics. This course is designed for beginners and at the end the students will get the ability to perform scientific computations and Simulink model development using XCos environment...
<b>Course Outcome:</b>	On successful completion of the course, the student shall be able to: CO.1. Describe the SCILAB software environment. CO.2. Discuss the concepts of basic SCILAB programming for engineering applications CO.3. Demonstrate the implementation of Xcos Simulink Environment CO.4. Interpret data from datasheet and to perform statistical analysis.
<b>Course Content:</b>	<b>Module No 1:</b> Introduction to Scilab environment- Scilab datatypes, variables and constants , Functions in Scilab Changing axes properties in scilab plots Plotting Bar graphs in Scilab [5- Hours] <b>Module No 2:</b> Entering Matrices and basic matrix operations of addition and multiplication transpose. Generating Matrices, the load Function, M-Files, Deleting Rows and Columns, Solving linear algebraic equations in Scilab. [8- Hours] <b>Module No 3:</b> Statistical computations, statistical functions- sum, mean value, median, Standard deviation. [9- Hours] <b>Module No 4:</b> Scilab toolboxes for the analyzing the systems, Solving Differential Equations in Xcos, Transfer function approach to solve ODEs. [8- Hours]
<b>Instructor In-charge:</b>	Dr. V Joshi Manohar





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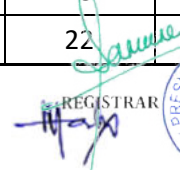
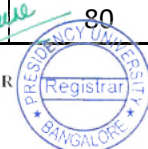
## School of Engineering Department of Electrical & Electronics Engineering

AY 2021-22 (Odd Sem)

Value added Course(VAC) Name and Code: Fundamentals of Scilab programming & EEEV011

Name of the Instructor: Dr. V Joshi Manohar

### Attendance Sheet

S.No.	STUDENT ID NO	STUDENT NAME	Total classes conducted	Total classes attended	Percentage attended
1	20181EEE0042	PRAJWAL S	30	27	90
2	20181EEE0044	PREETHAM HIMAKAR	30	27	90
3	20181EEE0045	PUNITH K KULAL	30	22	80
4	20181EEE0057	SHREE LAKSHMI G D	30	27	90
5	20181EEE0059	SONU B M	30	22	80
6	20181EEE0060	SOUMYA T	30	17	60
7	20181EEE0069	VISHNU T S	30	30	100
8	20181EEE0070	WASEELKHAN	30	22	80
9	20181EEE0071	YAMAVARAM MADHU SUDHAN	30	22	80
10	20191LEE0009	LOKESH. M	30	30	100
11	20191LEE0010	S R GURU PRASAD	30	22	80
12	20191LEE0011	RAKESH C S	30	27	90
13	20181EEE0002	ABHISHEK R BHARADWAJ	30	21	70
14	20181EEE0004	AMULYA A PUROHIT	30	22	80
15	20181EEE0005	ANKIT KUMAR SHARMA	30	15	50
16	20181EEE0014	DEEKSHITHA N	30	22	80
17	20181EEE0015	FARHAN MUKHTIAR ABDUL Late	30	22	80
18	20181EEE0016	SAIKUMAR G	30	22	80
19	20181EEE0023	JAYASHREE V	30	22	80
20	20181EEE0025	K SHRAVAN KUMAR	30	22	80
21	20181EEE0026	KALYAN CH	30	27	90
22	20181EEE9001	R TEJASWINI	30	30	80
23	20181EEE9002	H EMANTH H L	30	15	50
24	20191LEE0001	NIHARIKA H	30	22	80
Signature of Course Instructor			 REGISTRAR 		



**School of Engineering**  
**Department of Electrical & Electronics Engineering**  
**Value Added Course Marksheets**

Course Code :	EEEV011		Academic Year :			2021-22	
Course Name :	Fundamentals of Scilab programming		Semester :			Odd Semester	
			Instructor-in-Charge Name:			Dr. V Joshi Manohar	
			Instructor-in-Charge Employee ID :			PUNIV01153	
S. No	Roll No	Name	School SoE/SoL etc)	Attendance (in %)	Marks	Eligible for Certificate (Y/N)	Remark
1	20181EEE0042	PRAJWAL S	SoE	90	70	YES	
2	20181EEE0044	PREETHAM HIMAKAR	SoE	90	80	YES	
3	20181EEE0045	PUNITH K KULAL	SoE	80	70	YES	
4	20181EEE0057	SHREE LAKSHMI G D	SoE	90	50	YES	
5	20181EEE0059	SONU B M	SoE	80	65	YES	
6	20181EEE0060	SOUMYA T	SoE	60	Ab	NO	Not Eligible for Certificate
7	20181EEE0069	VISHNU T S	SoE	100	70	YES	
8	20181EEE0070	WASEELKHAN	SoE	80	53	YES	
9	20181EEE0071	YAMAVARAM MADHU SUDHAN	SoE	80	78	YES	
10	20191LEE0009	LOKESH. M	SoE	100	65	YES	
11	20191LEE0010	S R GURU PRASAD	SoE	80	60	YES	
12	20191LEE0011	RAKESH C S	SoE	90	70	YES	
13	20181EEE0002	ABHISHEK R BHARADWAJ	SoE	70	65	YES	
14	20181EEE0004	AMULYA A PUROHIT	SoE	80	30	YES	
15	20181EEE0005	ANKIT KUMAR SHARMA	SoE	50	Ab	NO	Not Eligible for Certificate
16	20181EEE0014	DEEKSHITHA N	SoE	80	80	YES	
17	20181EEE0015	FARHAN MUKHTIAR ABDUL Late	SoE	80	60	YES	



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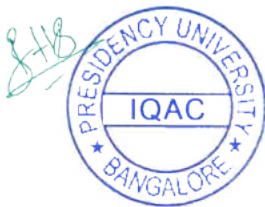
18	20181EEE0016	SAIKUMAR G	SoE	80	60	YES	
19	20181EEE0023	JAYASHREE V	SoE	80	80	YES	
20	20181EEE0025	K SHRAVAN KUMAR	SoE	80	50	YES	
21	20181EEE0026	KALYAN CH	SoE	90	30	YES	
22	20181EEE9001	R TEJASWINI	SoE	80	60	YES	
23	20181EEE9002	HEMANTH H L	SoE	50	AB	NO	Not Eligible for Certificate
24	20191LEE0001	NIHARIKA H	SoE	80	60	YES	

**Name of Course** Dr. V Joshi Manohar  
**Instructor :**  
**Employee ID of Course** PUNIV01211  
**Instructor:**

**Signature of Instructor-in-charge**

**Signature of HoD**

Head of the Department  
Electrical and Electronics Engineering  
School of Engineering  
PRESIDENCY UNIVERSITY  
Rajarajeshwari, Yalahanka, Bengaluru - 56





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**School of Engineering**  
**Department of Electrical & Electronics Engineering**

**Value Added Course offered during the Odd Semester AY 2021-22**

<b>Course Code:</b>	EEEV012
<b>Course Name:</b>	Synchronized Phasor measurement in Grid using PMUs
<b>Area of Specialization:</b>	Power System
<b>Course Description:</b>	This Course introduces to Phasor measurement unit (PMU) technology used for wide area grid monitoring to avoid blackout conditions. Advanced DSP algorithms are used to estimate the phasor value of voltage and current signals which helps in monitoring the dynamic nature of the power system. It develops analytical abilities in students with the help of Lab-VIEW Software.
<b>Course Outcome:</b>	On successful completion of the course, the student shall be able to: <b>01</b> Summarize the Power system Contingencies <b>02</b> Analyze phasor estimate for voltage and current for micro grid and conventional grid. <b>03</b> Explain the algorithm of recursive and non-recursive DFT <b>04</b> Compute and minimize the cost of reactive power consumption.
<b>Course Content:</b>	Module 1: Introduction to fourier transform, phasor estimation using discrete fourier transform method. [10 Hours]  Module 2: phasor estimation using non-recursive and recursive discrete fourier transform method in micro-grid and conventional grid, lab-view model and results using non-recursive DFT algorithm, lab-view model and results using recursive DFT algorithm [10 Hours]  Module 3: introduction to phasor measurement unit, hardware setup of phasor measurement unit and results, hardware setup using NI my-RIO, cost calculation on the basis of reactive energy consumption [10 Hours]
<b>Instructor In-charge:</b>	Mr Bishakh Paul





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**School of Engineering**  
**Department of Electrical & Electronics Engineering**  
**AY 2021-22 (Odd Sem)**

**Value added Course(VAC) Name and Code: Synchronized Phasor measurement in Grid using PMUs & EEEV012**




**Name of the Instructor: Mr Bishakh Paul**  
**Attendance Sheet**

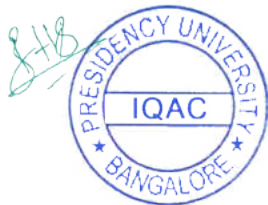
S.No.	STUDENT ID NO	STUDENT NAME	Total classes conducted	Total classes attended	Percentage attended
1	20181EEE0047	RAJATH KRISHNA R	30	24	80
2	20181EEE0048	RITHIK R K	30	23	75
3	20181EEE0051	SAI HRITHIK P K	30	22	75
4	20181EEE0061	SREEVATSA P M	30	23	75
5	20181EEE0063	SUMITH J JADHAV	30	22	80
6	20181EEE0064	SURABHI M Y	30	30	75
7	20181EEE0072	YASSER AHAMED KHAISAR	30	24	80
8	20181EEE0073	DILIPYADAV N	30	23	75
9	20181EEE0074	POOJA B S PATEL	30	23	75
10	20191LEE0012	ARJUN CHHETRY	30	0	0
11	20191LEE0013	NAGARAJU V	30	24	80
12	20191LEE9001	PRASHANTH RADHESYAM YADAV	30	23	75
13	20181EEE0006	ARSHAD SALEEM	30	30	75
14	20181EEE0007	ARVINDGOWDA C N	30	24	80
15	20181EEE0008	B H LAVANYA	30	24	80
16	20181EEE0017	G.D.S DHEERAJ	30	24	80
17	20181EEE0018	GATE MOHAMMAD SHADAB	30	24	80
18	20181EEE0019	HANUMANTH KUMAR A	30	24	80
19	20181EEE0028	KESHAV GANESH	30	24	80
20	20181EEE0031	MALLIKARJUN M HIREMATH	30	23	75
21	20181EEE0032	MANISH KUMAR	30	15	30
22	20191LEE0002	SHIVACHANDAN D L	30	24	80
23	20191LEE0003	BASAVAKUMAR S HIREHAL	30	15	50
24	20191LEE0004	RAVI KUMAR K	30	9	30



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25	20191LEE9003	VISHAK VIJAYA KUMAR	30	23	75
Signature of Course Instructor					





# PRESIDENCY UNIVERSITY

(Established under the Presidency University Act, 2013 of the Karnataka Act 41 of 2013)

**School of Engineering**  
**Department of Electrical & Electronics Engineering**  
**Value Added Course Marksheets**

Course Code :	EEEE012		Academic Year :			2021-22	
Course Name :	Synchronized Phasor measurement in Grid using PMUs		Semester :			Odd Semester	
			Instructor-in-Charge Name:			Mr Bishakh Paul	
			Instructor-in-Charge Employee ID :			PUNIV00895	
S. No	Roll No	Name	School (e.g. SoE/SoL etc)	Attendance (in %)	Marks	Eligible for Certificate (Y/N)	Remark
1	20181EEE0047	RAJATH KRISHNAR	SoE	80	90	YES	
2	20181EEE0048	RITHIK R K	SoE	75	80	YES	
3	20181EEE0051	SAI HRITHIK P K	SoE	75	80	YES	
4	20181EEE0061	SREEVATSA P M	SoE	75	75	YES	
5	20181EEE0063	SUMITH J JADHAV	SoE	80	80	YES	
6	20181EEE0064	SURABHI M Y	SoE	75	80	YES	
7	20181EEE0072	YASSER AHAMED KHAISAR	SoE	80	75	YES	
8	20181EEE0073	DILIPYADAV N	SoE	75	75	YES	
9	20181EEE0074	POOJA B S PATEL	SoE	75	75	YES	
10	20191LEE0012	ARJUN CHHETRY	SoE	0	AB	NO	Not Eligible for Certificate
11	20191LEE0013	NAGARAJU V	SoE	80	85	YES	
12	20191LEE0001	PRASHANTH RADHESYAM YADAV	SoE	75	80	YES	
13	20181EEE0006	ARSHAD SALEEM	SoE	75	AB	NO	Not Eligible for Certificate
14	20181EEE0007	ARVINDGOWDA C N	SoE	80	85	YES	
15	20181EEE0008	B H LAVANYA	SoE	80	90	YES	
16	20181EEE0017	G.D.S DHEERAJ	SoE	80	90	YES	



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(Established under the Presidency University Act, 2013 of the Karnataka Act 41 of 2013)

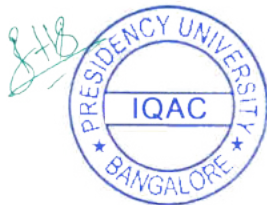
17	20181EEE0018	GATE MOHAMMAD SHADAB	SoE	80	85	YES	
18	20181EEE0019	HANUMANTH KUMAR A	SoE	80	AB	NO	Not Eligible for Certificate
19	20181EEE0028	KESHAV GANESH	SoE	80	AB	NO	Not Eligible for Certificate
20	20181EEE0031	MALLIKARJUN M HIREMATH	SoE	75	AB	NO	Not Eligible for Certificate
21	20181EEE0032	MANISH KUMAR	SoE	30	AB	NO	Not Eligible for Certificate
22	20191LEE0002	SHIVACHANDAN D L	SoE	80	80	YES	
23	20191LEE0003	BASAVAKUMAR S HIREHAL	SoE	50	AB	NO	Not Eligible for Certificate
24	20191LEE0004	RAVI KUMAR K	SoE	30	80	YES	
25	20191LEE9003	VISHAK VIJAYA KUMAR	SoE	75	AB	NO	Not Eligible for Certificate

Name of Course Instructor : Mr Bishakh Paul

Employee ID of Course Instructor: PUNIV00895

Signature of Instructor-in-charge

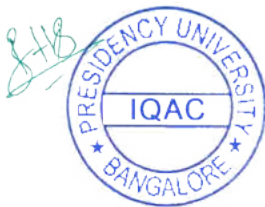
Signature of  
HoD  
Head of the Department  
Electrical and Electronics Engineering  
School of Engineering  
PRESIDENCY UNIVERSITY  
Kajjenukunte, Yalahanka, Bengaluru - 56





**School of Engineering**  
**Department of Electrical & Electronics Engineering**  
**Value Added Course offered during the Odd Semester 2021-2022**

<b>Course Code:</b>	EEEV013
<b>Course Name:</b>	Simulation of Power Electronics circuits using MATLAB Simulink, Python and LTSpice
<b>Area of Specialization:</b>	Electrcal Engineering
<b>Course Description:</b>	This course is designed to allow you to simulate any power electronics device in MATLAB/Simulink and LT Spice including rectifiers, dc-to-dc converters, and inverters. The course also has a basic introduction on Python programming to help you with writing control code for electrical circuits. The course uses the free and open source circuit simulator Python Power Electronics. After going through this course, you will be able to create professional electrical power converter circuits for Electric vehicle applications.
<b>Course Outcome:</b>	On successful completion of the course, the student shall be able to: CO.1.Recognize the importance of MATLAB, LT spice and its capabilities CO.2.Explain the simulation of AC-DC circuits using Simulink in MATLAB and LT spice. CO.3.Explain the simulation DC-DC and DC-AC converters using Simulink in MATLAB CO.4.Show the power electronics model for EV battery charging usingMATLAB Simulink
<b>Course Content:</b>	<b>Module No 1:</b> Basics of MATLAB Simulink,Generation of dfferent signals,phase delay, duty ratio of pulse Generators, Introduction to LT spice [5- Hours] <b>Module No 2:</b> MATLAB Simulink of AC-DC Converters, Single phase half wave, full wave, semi controlled and uncontrolled rectifiers, three phase full wave controlled rectifiers with different loads [8- Hours] <b>Module No 3:</b> MATLAB Simulink of DC-DC Converters, buck, boost converters, single phase inverters, three phase inverters with different load conditions, pulse width modulation techniques. [9- Hours] <b>Module No 4:</b> Buck converter using LT Spice, introduction to python, Basic RLC circuits using python, bidirectional converter model for charging battery of EV and current scenario of power electronic applications. [8- Hours]
<b>Instructor In-charge:</b>	Mr. K Srekanth Reddy







**School of Engineering**  
**Department of Electrical & Electronics Engineering**

AY 2021-22 (Odd Sem)

Value added Course(VAC) Name and Code: Simulation of Power Electronics circuits using MATLAB Simulink, Python and LTSpice & EEEV013

Name of the Instructor: Mr. K Sreekanth Reddy


**Attendance Sheet**

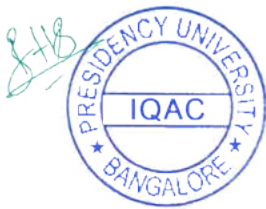
S.No.	STUDENT ID NO	STUDENT NAME	Total classes conducted	Total classes attended	Percentage attended
1	20211LEE0017	NARESH R N	30	27	90.00
2	20191EEE0005	ASFIYA AAZIM	30	27	90.00
3	20191EEE0045	SIVA PRASAD L	30	24	80.00
4	20191EEE0050	YARRABALLI NAVEEN	30	27	90.00
5	20211LEE0021	CHARAN P	30	24	80.00
6	20191EEE0018	MOHAMMED NOORUDDIN ASRAR	30	18	60.00
7	20191EEE0044	SHWETHA N	30	30	100.00
8	20191EEE0057	ZAID AHMED ZAUED HAMADAH	30	24	80.00
9	20191EEE0037	SANJAY B	30	24	80.00
10	20211LEE0016	NIRANJAN JAGADISH PAMMAR	30	30	100.00
11	20211LEE0014	AMBIKA M BIJAPUR	30	24	80.00
12	20211LEE0013	CHARANREDDY S V	30	27	90.00
13	20211LEE0009	PAVAN V	30	21	70.00
14	20191EEE0023	NAVYA N	30	24	80.00
15	20191EEE0019	MOKA ABHINASH	30	15	50.00
16	20191EEE0004	ARUN S	30	24	80.00
17	20191MEC0132	RESAPU LIKHITH REDDY	30	24	80.00
18	20201LEE0011	Mamilla Girish reddy mamilla	30	24	80.00
19	20191EEE0042	SHARANYA P C	30	24	80.00
20	20191EEE0060	NAVEEN NELSON W	30	24	80.00
21	20191EEE0022	NANDA KISHORE KIRAN DESHPANDE	30	24	80.00
22	20201EEE0003	SHRAVANI N	30	24	80.00
2	20201EEE0021	JILLIVARI KURUVA PRASAD	30	15	50.00



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24	20191EEE0003	ANUSHA M JOLAD	30	09	30.00
25	20191EEE0016	MOHAMMAD JAMEEL	30	0	0.00
26	20191EEE0051	YASHASH	30	0	0.00
27	20201LEE0009	BHARATH KUMAR S	30	0	0.00
28	20211LEE0025	DARSHAN	30	0	0.00
<b>Signature of Course Instructor</b>					



**School of Engineering**  
**Department of Electrical & Electronics Engineering**  
**Value Added Course Marksheet**

Course Code :	EEEEV013		Academic Year :			2021-22	
Course Name :	Simulation of Power Electronics circuits using MATLAB Simulink, Python and LTSpice		Semester :			Odd Semester	
			Instructor-in-Charge Name:			Mr. K Sreekanth Reddy	
			Instructor-in-Charge Employee ID :			PUNIV00489	
S. No	Roll No	Name	School SoE/SoL etc)	Attendance (in %)	Marks	Eligible for Certificate (Y/N)	Remarks
1	20211LEE0017	NARESH R N	SoE	90	70	Yes	
2	20191EEE0005	ASFIYA AAZIM	SoE	90	80	Yes	
3	20191EEE0045	SIVA PRASAD L	SoE	80	70	Yes	
4	20191EEE0050	YARRBALLI NAVEEN	SoE	90	50	Yes	
5	20211LEE0021	CHARAN P	SoE	80	65	Yes	
6	20191EEE0018	MOHAMMED NOORUDDIN ASRAR	SoE	60	Ab	No	Not eligible for certificate
7	20191EEE0044	SHWETHA N	SoE	100	70	Yes	
8	20191EEE0057	ZAID AHMED ZAUED HAMADAH	SoE	80	21	No	Not eligible for certificate
9	20191EEE0037	SANJAY B	SoE	80	78	Yes	
10	20211LEE0016	NIRANJAN JAGADISH PAMMAR	SoE	100	65	Yes	
11	20211LEE0014	AMBIKA M BIJAPUR	SoE	80	60	Yes	
12	20211LEE0013	CHARANREDDY S V	SoE	90	70	Yes	
13	20211LEE0009	PAVAN V	SoE	70	65	Yes	
14	20191EEE0023	NAVYA N	SoE	80	30	No	Not eligible for certificate
15	20191EEE0019	MOKA ABHINASH	SoE	50	Ab	No	Not eligible for certificate
16	20191EEE0004	ARUN S	SoE	80	80	Yes	



# PRESIDENCY UNIVERSITY

(Established under the Presidency University Act, 2013 of the Karnataka Act 41 of 2013)

17	20191MEC0132	RESAPU LIKHITH REDDY	SoE	80	60	Yes	
18	20201LEE0011	MAMILLA GIRISH REDDY MAMILLA	SoE	80	60	Yes	
19	20191EEE0042	SHARANYA P C	SoE	80	80	Yes	
20	20191EEE0060	NAVEEN NELSON W	SoE	80	50	Yes	
21	20191EEE0022	NANDA KISHORE KIRAN DESHPANDE	SoE	30	30	No	Not eligible for certificate
22	20201EEE0003	SHRAVANI N	SoE	80	60	Yes	
2	20201EEE0021	JILLIVARI KURUVA PRASAD	SoE	50	43	No	Not eligible for certificate
24	20191EEE0003	ANUSHA M JOLAD	SoE	30	40	No	Not eligible for certificate
25	20191EEE0016	MOHAMMAD JAMEEL	SoE	0	0	No	Not eligible for certificate
26	20191EEE0051	YASHASH	SoE	0	60	No	Not eligible for certificate
27	20201LEE0009	BHARATH KUMAR S	SoE	0	Ab	No	Not eligible for certificate
28	20211LEE0025	DARSHAN	SoE	0	Ab	No	Not eligible for certificate

**Name of Course**

**Instructor :**

**Employee ID of Course**

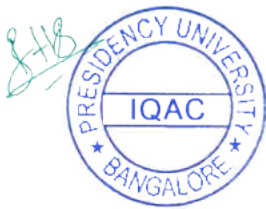
**Instructor:**

Mr. K Sreekanth Reddy

PUNIV00489

*K. Sreekanth Reddy*

**Signature of Instructor-  
in-charge**



*[Handwritten Signature]*

**Signature of HoD**

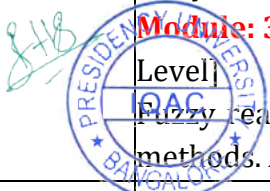
Head of the Department,  
Electrical and Electronics Engineering  
School of Engineering  
PRESIDENCY UNIVERSITY  
Kogalur, Yalahanka, Bengaluru -64

*[Handwritten Signature]*  
REGISTRAR  
PRESIDENCY UNIVERSITY  
BANGALORE

## Department of Electrical & Electronics Engineering

### Value Added Courses to be offered during the ODD Semester 2021-2022

<b>Course Code:</b>	EEEV017
<b>Course Name:</b>	Design of Fuzzy Logic Systems
<b>Area of Specialization:</b>	Soft Computing Techniques
<b>Course Description:</b>	In this course, an intelligent technique for data processing drawn from complex and imprecise environment are presented and studied. Fuzzy Logic theory is based on the empirical aspect of the human reasoning, and is used in the manipulation of imperfect, imprecise or approximate knowledge. It allows the modeling and processing of very complex systems in which, for example, human factors are present. Theory and applications concerning fuzzy logic exist for more than fifty years. It covers several fields such as artificial intelligence, identification and control of dynamic systems, automatic decision-making in complex systems, and fault diagnosis in industrial processes.
<b>Course Outcome:</b>	On successful completion of the course, the student shall be able to:  CO1: Define fuzzy logic theory and membership functions CO2: Discuss operations and properties of fuzzy systems CO3: Describe fuzzy relations and fuzzy rules CO4: Describe fuzzy logic control operations and applications
<b>Course Content:</b>	<p><b>Module: 1: Introduction to fuzzy set</b> [10 Hrs] [Knowledge Level] Introduction of Fuzzy Logic techniques to manipulate imprecise and approximated data and systems - Fuzzy sets theory and concepts, membership functions, operations on fuzzy sets, triangular norms</p> <p><b>Module: 2: Fuzzy operations</b> [10 Hrs] [Application Level] Fuzzy relations and fuzzy quantities, fuzzy intervals, fuzzy numbers, operation on fuzzy quantities, Linguistic variables, linguistic modifiers, fuzzy rules, fuzzy quantifiers</p> <p><b>Module: 3: Fuzzy reasoning</b> [12 Hrs] [Application Level] Fuzzy reasoning, fuzzy implications, Fuzzy control, Mamdani and Larsen methods. Applications - MATLAB simulations using the Fuzzy Toolbox</p>
<b>Instructor In-charge:</b>	Mr. Sarin MV



**Presidency University, Bengaluru**  
**Department of Electrical & Electronics Engineering**  
**School of Engineering**

**VAC DETAILS**

**Total number of hours:30**

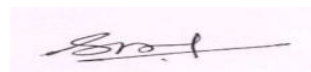
**AY 2021-2022(Winter Sem)**

**Value added Course(VAC) Name and Code: EEEV017 Design of Fuzzy Logic Systems**

**Name of the Instructor: Mr. Sarin MV**

S.No.	STUDENT ID NO	STUDENT NAME	Total classes conducted	Total classes attended	Percentage attended
1	20181EEE0010	BATHALA PRASHANTH	30	28	93
2	20181EEE0018	GATE MOHAMMAD SHADAB	30	25	83
3	20181EEE0036	PAGADALA REVANTH NATH	30	25	83
4	20181EEE0037	PALLA REDDAIAH	30	25	83
5	20181EEE0039	PANDLA GURU SAI GOUD	30	28	93
6	20181EEE0050	SADIYA TAHERA	30	25	83
7	20181EEE0054	SANJAY KUMAR S C	30	28	93
8	20181EEE0067	TEJAS GOWDA M	30	25	83
9	20181EEE0071	YAMAVARAM MADHU SUDHAN	30	25	83
10	20181EEE0073	DILIPYADAV N	30	0	0
11	20191EEE0001	ABHISHEK C	30	24	80
12	20191EEE0006	ASHISH SINGH BHUMIJ	30	27	90
13	20191EEE0010	EASHWAR V	30	23	77
14	20191EEE0012	KOMALA M E	30	26	87
15	20191EEE0013	KOTHAKOTA JAI RAMAKRISHNA	30	26	87
16	20191EEE0015	MANDADI KARTHIKEYAN REDDY	30	26	87
17	20191EEE0016	MOHAMMAD JAMEEL	30	26	87
18	20191EEE0017	MOHAMMAD ZAID FAROOQ	30	26	87
19	20191EEE0024	NAVYA SHREE M	30	26	87
20	20191EEE0025	P ABHINAV	30	24	80
21	20191EEE0032	R S SHARUKH	30	15	50
22	20191EEE0039	SANKET VIJAY KUMAR KAMBLE	30	19	63
23	20191EEE0041	SHAIK MUNEEER	30	14	47
24	20191EEE0047	Srinidhi R	30	12	40
25	20191EEE0049	VARSHA B N	30	24	80
26	20191LEE0009	Lokesh M	30	16	53
27	20191LEE0013	Nagaraju v	30	8	27
28	20191LEE9001	Prashant Radheshyam Yadav	30	0	0
29	20201EEE0005	Rakshitha.B	30	19	63
30	20201EEE0018	FIZA	30	14	47
31	20201EEE0022	YASHASWINI BG	30	12	40
32	20211LEE0004	YOGENDRA	30	0	0
33	20211LEE0011	KISHORE TEJA S N	30	0	0
34	20191LEE0005	KUSHAL S	30	26	87
35	20191LEE0008	ANANDHENDRA HD	30	0	0
36	20191LEE0011	RAKESH LEE	30	0	0
37	20191LEE0010	S R GURUPRASAD	30	0	0
38	20191EEE0035	Sagar .B	30	0	0

**Signature of Course Instructor**



REGISTRAR





**Presidency University, Bengaluru**  
**Value Added Course Marksheet**  
**School of Engineering**

Code : EEEV017		Academic Year :			2021-2022		
Course Name : Design of Fuzzy Logic Systems		Semester :			Odd Semester		
		Instructor-in-Charge Name :			Mr. Sarin MV		
		Instructor-in-Charge Employee ID :			PUNIV01347		
S. No	Roll No	Name	School SoE	Attendance (in %)	Marks out of 100	Eligible for Certificate (Yes/No)	Remark
1	20181EEE0010	BATHALA PRASHANTH	SoE	93	90	YES	
2	20181EEE0018	GATE MOHAMMAD SHADAB	SoE	83	80	YES	
3	20181EEE0036	PAGADALA REVANTH NATH	SoE	83	80	YES	
4	20181EEE0037	PALLA REDDAIAH	SoE	83	75	YES	
5	20181EEE0039	PANDLA GURU SAI GOUD	SoE	93	80	YES	
6	20181EEE0050	SADIYA TAHERA	SoE	83	80	YES	
7	20181EEE0054	SANJAY KUMAR S C	SoE	93	75	YES	
8	20181EEE0067	TEJAS GOWDA M	SoE	83	75	YES	
9	20181EEE0071	YAMAVARAM MADHU SUDHAN	SoE	83	75	YES	
10	20181EEE0073	DILIPYADAV N	SoE	0	AB	No	not eligible for certificate
11	20191EEE0001	ABHISHEK C	SoE	80	85	YES	
12	20191EEE0006	ASHISH SINGH BHUMIJ	SoE	90	80	YES	
13	20191EEE0010	EASHWAR V	SoE	77	AB	No	not eligible for certificate
14	20191EEE0012	KOMALA M E	SoE	87	85	YES	
15	20191EEE0013	KOTHAKOTA JAI RAMAKRISHNA	SoE	87	90	YES	
16	20191EEE0015	MANDADI KARTHIKEYAN REDDY	SoE	87	90	YES	
17	20191EEE0016	MOHAMMAD JAMEEL	SoE	87	85	YES	
18	20191EEE0017	MOHAMMAD ZAID FAROOQ	SoE	87	AB	No	not eligible for certificate
19	20191EEE0024	NAVYA SHREE M	SoE	87	AB	No	not eligible for certificate
20	20191EEE0025	P ABHINAV	SoE	80	AB	No	not eligible for certificate
21	20191EEE0032	R S SHARUKH	SoE	50	AB	No	not eligible for certificate
22	20191EEE0039	SANKET VIJAY KUMAR KAMBLE	SoE	63	30	NO	not eligible for certificate
23	20191EEE0041	SHAIK MUNEEER	SoE	47	AB	No	not eligible for certificate
24	20191EEE0047	Srinidhi R	SoE	40	40	NO	not eligible for certificate
25	20191EEE0049	VARSHA B N	SoE	80	AB	No	not eligible for certificate
26	20191lee0009	Lokesh M	SoE	53	AB	No	not eligible for certificate
27	20191LEE0013	Nagaraju v	SoE	27	AB	No	not eligible for certificate
28	20191LEE9001	Prashant Radheshyam Yadav	SoE	0	AB	No	not eligible for certificate
29	20201EEE0005	Rakshitha.B	SoE	63	80	YES	
30	20201EEE0018	FIZA	SoE	47	30	No	not eligible for certificate
31	20201EEE0022	YASHASWINI BG	SoE	40	30	No	not eligible for certificate
32	20211LEE0004	YOGENDRA	SoE	0	AB	No	not eligible for certificate
33	20211LEE0011	KISHORE TEJA S N	SoE	0	AB	No	not eligible for certificate
34	20191LEE0005	KUSHAL S	SoE	87	AB	No	not eligible for certificate
35	20191LEE0008	PANCHENDRA HD	SoE	0	AB	No	not eligible for certificate
36	20191LEE0011	RAKESH LEE	SoE	0	AB	No	not eligible for certificate
37	20191LEE0010	S R GURUPRASAD	SoE	0	AB	No	not eligible for certificate
38	20191EEE0035	Sagar .B	SoE	0	AB	No	not eligible for certificate

Name of Course Instructor 1: Mr. Sarin MV  
Employee ID of Course Instructor 1: PUNIV01347

Signature of Course Instructor

  
Head of the Department,  
Electrical and Electronics Engineering  
School of Engineering  
PRESIDENCY UNIVERSITY  
Bijayanur, Yelahanka, Bengaluru - 56

  
REGISTRAR





**School of Engineering**  
**Department of Electrical & Electronics Engineering**  
**Value Added Course offered during the Odd Semester 2021-2022**

<b>Course Code:</b>	EEEV018
<b>Course Name:</b>	Introduction to MATLAB/Simulink
<b>Area of Specialization:</b>	Power Electronics, Control Systems
<b>Course Description:</b>	This course gives an introduction to MATLAB programming language and Simulink toolbox which is a graphical extension of MATLAB. This technical computing environment helps the students to involve in more intense problem solving applications and provides an opportunity to tackle realistic and more complicated problems.
<b>Course Outcome:</b>	On successful completion of the course, the student shall be able to: CO.1. Discuss the basic features of MATLAB development environment. CO.2. Summarize the MATLAB Graphical user interface. CO.3. Explain various toolboxes in engineering applications. CO.4. Construct Simulink block diagram for the given physical system.
<b>Course Content:</b>	<b>Module No 1:</b> Introduction to MATLAB, how to create variables in MATLAB, constructing M.Files, matrix operations, control flow, various arithmetic and logical operators, graphics, debugging of M.Files [8- Hours] <b>Module No 2:</b> Introduction to Simulink, various Simulink libraries, constructing Simulink block diagram [8- Hours] <b>Module No 3:</b> Toolboxes available in MATLAB: Control system toolbox, Simpower systems toolbox, Fuzzy logic toolbox, Neural network toolbox. [8- Hours] <b>Module No 4:</b> Simulation of various electrical and electronic circuits and block diagrams using Simulink [6- Hours]
<b>Instructor In-charge:</b>	Ms Jisha L K







**School of Engineering  
Department of Electrical & Electronics Engineering  
AY 2021-22 (Odd Sem)**

**Value added Course(VAC) Name and Code: Introduction to MATLAB/Simulink & EEEV018  
Name of the Instructor: Ms. Jisha L K**

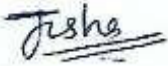
**Attendance Sheet**

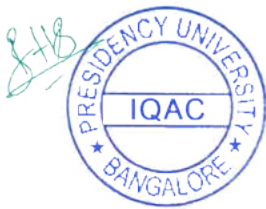
S.No.	STUDENT ID NO	STUDENT NAME	Total classes conducted	Total classes attended	Percentage attended
1	20181EEE0052	SAICHARAN T A	30	22	73.33
2	20181EEE0053	SAMI UL ARFAATH	30	0	0.00
3	20181EEE0055	SATISH KUMAR	30	25	83.33
4	20181EEE0066	SYED IDREES QUADRI	30	22	73.33
5	20181EEE0067	TEJAS GOWDA M	30	28	80.00
6	20181EEE0068	VARSHA ANIL P	30	25	80.00
7	20181EEE0075	T POORNIMA	30	25	80.00
8	20181EEE9003	SYED ZABI SAMEER	30	30	100
9	20181EEE9006	KRUTHAN KRISHNA SWAMY	30	28	90.00
10	20191LEE9002	N SHIVA TULSHI KUMAR	30	30	100
11	20191LEE9004	SANDEEP KUMAR	30	30	100
12	20181EEE0001	ABHINAV SURESH	30	27	90.00
13	20181EEE0010	BATHALA PRASHANTH	30	23	80.00
14	20181EEE0012	BOGGULA MARUTHI MANOHAR REDDY	30	30	100
15	20181EEE0013	CHARAN KUMAR N	30	30	100
16	20181EEE0020	HARIKRISHN V	30	27	90.00
17	20181EEE0021	JAHNAVI J P	30	27	90.00
18	20181EEE0022	JAYASHREE SAHU	30	30	100
19	20181EEE0034	MOHAMMED NASIR	30	23	76.67
20	20181EEE0036	PAGADALA REVANTH NATH	30	23	76.67
21	20181EEE0037	PALLA REDDAIAH	30	23	76.67
22	20191LEE0005	KUSHAL S	30	22	73.33
23	20191LEE0006	DHEERAJ C	30	23	80.00



# PRESIDENCY UNIVERSITY

(Established under the Presidency University Act, 2013 of the Karnataka Act 41 of 2013)

24	20181EEE9007	MD KAISE	30	23	80.00
25	20181EEE9008	BHARATH S	30	24	83.33
Signature of Course Instructor					





# PRESIDENCY UNIVERSITY

(Established under the Presidency University Act, 2013 of the Karnataka Act 41 of 2013)

## School of Engineering Department of Electrical & Electronics Engineering

### Value Added Course Marksheets

<b>Course Code :</b>	<b>EEEV018</b>		<b>Academic Year :</b>			<b>2021-22</b>	
<b>Course Name :</b>	<b>Introduction to MATLAB/Simulink</b>		<b>Semester :</b>			<b>Odd Semester</b>	
			<b>Instructor-in-Charge Name:</b>			<b>Ms. Jisha L K</b>	
			<b>Instructor-in-Charge Employee ID :</b>			<b>PUNIV01427</b>	
<b>S. No</b>	<b>Roll No</b>	<b>Name</b>	<b>School SoE/SoL etc)</b>	<b>Attendance (in %)</b>	<b>Marks</b>	<b>Eligible for Certificate (Y/N)</b>	<b>Remark</b>
1	20181EEE0052	SAICHARAN T A	SoE	73.33	62.5	YES	
2	20181EEE0053	SAMI UL ARFAATH	SoE	0.00	Ab	NO	Not eligible for certificate
3	20181EEE0055	SATISH KUMAR	SoE	83.33	90	YES	
4	20181EEE0066	SYED IDREES QUADRI	SoE	73.33	60	YES	
5	20181EEE0067	TEJAS GOWDA M	SoE	80.00	42.5	YES	
6	20181EEE0068	VARSHA ANIL P	SoE	80.00	55	YES	
7	20181EEE0075	T POORNIMA	SoE	80.00	72.5	YES	
8	20181EEE9003	SYED ZABI SAMEER	SoE	100	65	YES	
9	20181EEE9006	KRUTHAN KRISHNA SWAMY	SoE	90.00	Ab	NO	Not eligible for certificate
10	20191LEE9002	N SHIVA TULSHI KUMAR	SoE	100	72.5	YES	
11	20191LEE9004	SANDEEP KUMAR	SoE	100	75	YES	
12	20181EEE0001	ABHINAV SURESH	SoE	90.00	62.5	YES	
13	20181EEE0010	BATHALA PRASHANTH	SoE	80.00	45	YES	
14	20181EEE0012	BOGGULA MARUTHI MANOHAR REDDY	SoE	100	62.5	YES	
15	20181EEE0013	CHARAN KUMAR N	SoE	100	40	YES	
16	20181EEE0020	HARIKRISHN V	SoE	90.00	50	YES	
17	20181EEE0021	JAHNAVI J P	SoE	90.00	67.5	YES	



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18	20181EEE0022	JAYASHREE SAHU	SoE	100	47.5	YES	
19	20181EEE0034	MOHAMMED NASIR	SoE	76.67	60	YES	
20	20181EEE0036	PAGADALA REVANTH NATH	SoE	76.67	57.5	YES	
21	20181EEE0037	PALLA REDDAIAH	SoE	76.67	55	YES	
22	20191LEE0005	KUSHAL S	SoE	73.33	60	YES	
23	20191LEE0006	DHEERAJ C	SoE	80.00	42.5	YES	
24	20181EEE9007	MD KAISE	SoE	80.00	55	YES	
25	20181EEE9008	BHARATH S	SoE	83.33	90	YES	

**Name of Course**

**Ms. Jisha L K**

**Instructor :**

**Employee ID of Course**

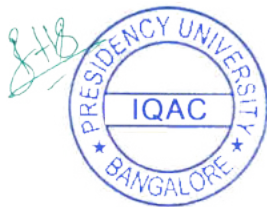
**PUNIV01427**

**Instructor:**

**Signature of  
Instructor-in-charge**

**Signature of HoD**

Head of the Department,  
Electrical and Electronics Engineering  
School of Engineering  
PRESIDENCY UNIVERSITY  
Kogaluramk, Yalahanka, Bengaluru -54





# **PRESIDENCY UNIVERSITY**

(Private University Estd. in Karnataka State by Act No. 41 of 2013)

## **PU-SoE-EEE 2021-22**

Ref. No. PU/ SoE/ EEE /2021-22/VAC/CIR/02

11-05-2022

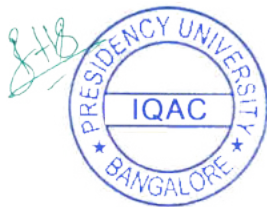
### **CIRCULAR**

#### **Sub: VALUE ADDED COURSES – OFFERED BY THE DEPT. OF EEE**

This is to inform all the students of the 2nd, 4th, and 8th semesters of B. Tech (EEE), the following value-added courses will be offered by the department during the AY 2021-22 (Winter Semester):

Sl. No	Course Code	Course Name	Name of the Faculty
1.	EEEV005	AutoCAD for Electrical Engineers	Mr. Ravi V Angadi
2.	EEEV011	Introduction to Scilab for Engineers	Dr. V Joshi Manohar
3.	EEEV012	Synchronized phasor measurement in Grid using PMUs	Mr. Bishakh Paul
4.	EEEV016	Design and simulation of DC-DC converters	Mrs. Ragasudha C P
5.	EEEV017	Design of Fuzzy Logic Systems	Mr. Sarin M V
6.	EEEV018	Introduction to MATLAB Simulink	Dr. Jisha L K

All are informed to contact the respective course ICs of VAC based on your choice. The duration of the course is 30 hours. All the students are encouraged to attend VAC as per the course instructor's schedule. A certificate will be awarded after successful completion of the course.



Dr. Joshi Manohar  
HOD - EEE



**City Office: University House, 8/1, King Street, Richmond Town, Bengaluru - 560025**

**Campus: Presidency University, Itgalpura, Rajanukunte, Bengaluru - 560064**

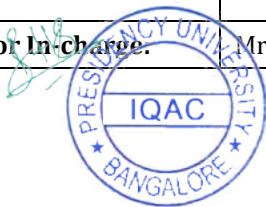
**Phone: + 80 4925 5533 / 5599 Email ID: [info@presidencyuniversity.in](mailto:info@presidencyuniversity.in)**

[www.presidencyuniversity.in](http://www.presidencyuniversity.in)



**School of Engineering**  
**Department of Electrical & Electronics Engineering**  
**Value Added Course offered during the Even Semester 2021-22**

<b>Course Code:</b>	EEEV005
<b>Course Name:</b>	Auto CAD for Electrical Engineers
<b>Area of Specialization:</b>	Electrcal Engineering
<b>Course Description:</b>	This course contains a detailed explanation of AutoCAD Electrical tools and features. Every tool and feature is thoroughly explained with the help of examples. After going through this course, you will be able to create professional electrical control drawings with ease such as ladder diagrams, schematic drawings, panel drawings, parametric and nonparametric PLC modules, point-to-point wiring diagrams, report generation, creation of symbols, Circuit Builder, Terminal symbols, and so on.
<b>Course Outcome:</b>	On successful completion of the course, the student shall be able to: CO.1. Explain all AutoCAD Electrical tools and features CO.2. Develop professional electrical control drawings with ease. CO.3. Create a Panel Drawings, Wiring Diagram and creation of symbol. CO.4. Explain the various types of wire selection and PLC selection in CAD.
<b>Course Content:</b>	<b>Module No 1:</b> Basics Of Electrical Drawings: Introduction, Need of Drawings, Electrical Drawings, Common Symbols in Electrical Drawings, Wire and its Types, Labeling. [5- Hours] <b>Module No 2:</b> Introduction to AutoCAD Electrical and Interface: Introduction, System Requirement, Starting AutoCAD Electrical/AutoCAD, Creating A New Drawing Document, Meaning of Default templates, Electrical Templates, Application Menu. Starting Drawing, Open Options, Opening Drawing File Save, Applying Password on File, Save As, Export, Publish, Print Drawing Tab Bar, Drawing Area, Command Window, Bottom Bar, Drafting Settings dialog box [8- Hours] <b>Module No 3:</b> Project Management: Introduction, Project Management, Workflow in AutoCAD Electrical, Starting a New Project, Changing Properties of a project, Adding drawings in the project, Retagging and renumbering ladders in the drawings of project, Plotting/publishing project files, INSERTING COMPONENTS: Inserting Components using Icon menu, Inserting Components using Catalog Browser, Inserting Components using User Defined list, Inserting Components using Equipment list, Inserting Components using Panel list, Inserting Components using Terminal (Panel list), Pneumatic, Hydraulic, and P&ID components [9- Hours] <b>Module No 4:</b> Wires, Circuits, and Ladders: Inserting Wires, Applying wire numbers, Inserting user defined circuits, Inserting ladders, Cable Markers, Circuit Builders. Plcs and Components: Introduction, Application of PLCs in manufacturing process, Inserting Parametric PLCs, Inserting PLCs (Full Unit), Inserting Connectors, Inserting Terminals. [8- Hours]
<b>Instructor In-charge:</b>	Mr. Ravi V Angadi





**School of Engineering**  
**Department of Electrical & Electronics Engineering**  
**AY 2021-22 (Even Sem)**

**Value added Course(VAC) Name and Code: Auto CAD for Electrical Engineers & EEEV005**

**Name of the Instructor: Mr. Ravi V Angadi**

**Attendance Sheet**

S.No.	STUDENT ID NO	STUDENT NAME	Total classes conducted	Total classes attended	Percentage attended
1	20191EEE0032	R S SHARUKH	30	28	93.33
2	20191EEE0035	SAGAR .B	30	6	20.00
3	20191EEE0041	SHAIK MUNEER	30	24	80.00
4	20191EEE0049	VARSHA B N	30	2	6.67
5	20191EEE9003	SRINIVAS K	30	6	20.00
6	20201EEE0003	SHRAVANI N	30	28	93.33
7	20201EEE0012	G YOGESHWARAN	30	24	80.00
8	20201EEE0021	JILLIVARI KURUVA PRASAD	30	24	80.00
9	20201EEE0026	MANJUNATH K	30	0	0.00
10	20201LEE0004	PRAVEEN M	30	0	0.00
11	20211EEE0001	PENUGONDA CHARAN	30	28	93.33
12	20211EEE0002	SHAIK AHAMMAD	30	30	100.00
13	20211EEE0006	PIYUSH NISHAD	30	24	80.00
14	20211EEE0018	ROHAN R	30	6	20.00
15	20211EEE0023	MASROOR AHMED	30	0	0.00
16	20211EEE0024	ANIRUDH S	30	24	80.00
17	20211EEE0025	RATHISH HOMBALE N	30	24	80.00
18	20211EEE0027	YASHWANTH KUMAR S	30	22	73.33
19	20211EEE0029	CHEZHAN S KATTI	30	24	80.00
20	20211EEE0033	MOHAMMAD NABEEL ABBAS	30	22	73.33
21	20211EEE0034	RAJANEESH B S	30	24	80.00
22	20211EEE0035	V RAHUL BALAJIGA	30	24	80.00
23	20211EEE0036	DEEPAK DANIEL F	30	24	80.00
24	20211EEE0038	HEMANT PANDIT	30	28	93.33
25	20211EEE0039	AKASH K	30	8	26.67
26	20211EEE0041	NAYANI POORNACHANDAN ROYAL	30	24	80.00
27	20211EEE0044	MOHAMMED ABRAR	30	24	80.00
28	20211LEE0046	BASIL BINU	30	4	13.33
29	20211EEE0050	BUDURI YASWANTH	30	25	86.67
30	20211LEE0006	PRABHAS M	30	0	0.00
31	20211LEE0015	NAGENDRA B	30	0	0.00
32	20211LEE0019	G TARUN	30	0	0.00
<b>Signature of Course Instructor</b>					



**School of Engineering**  
**Department of Electrical & Electronics Engineering**  
**Value Added Course Marksheet**

Course Code :	EEEEV005		Academic Year :			2021-22	
Course Name :	AutoCAD for Electrical Engineers		Semester :			Even Semester	
			Instructor-in-Charge Name:			Mr. Ravi V Angadi	
			Instructor-in-Charge Employee ID :			PUNIV01021	
S. No	Roll No	Name	School SoE/SoL etc)	Attendance (in %)	Marks	Eligible for Certificate (Y/N)	Remark
1	20191EEE0032	R S SHARUKH	SoE	93.33	70	Yes	
2	20191EEE0035	SAGAR .B	SoE	20	Ab	No	Not Eligible for Certificate
3	20191EEE0041	SHAIK MUNEER	SoE	80	52	Yes	
4	20191EEE0049	VARSHA B N	SoE	6.67	Ab	No	Not Eligible for Certificate
5	20191EEE9003	SRINIVAS K	SoE	20	Ab	No	Not Eligible for Certificate
6	20201EEE0003	SHRAVANI N	SoE	93.33	60	Yes	
7	20201EEE0012	G YOGESHWARAN	SoE	80	50	Yes	
8	20201EEE0021	JILLIVARI KURUVA PRASAD	SoE	80	78	Yes	
9	20201EEE0026	MANJUNATH K	SoE	0	Ab	No	Not Eligible for Certificate
10	20201LEE0004	PRAVEEN M	SoE	0	Ab	No	Not Eligible for Certificate
11	20211EEE0001	PENUGONDA CHARAN	SoE	93.33	50	Yes	
12	20211EEE0002	SHAIK AHAMMAD	SoE	100	52	Yes	
13	20211EEE0006	PIYUSH NISHAD	SoE	80	64	Yes	
14	20211EEE0015	ROHAN R	SoE	20	Ab	No	Not Eligible for Certificate
15	20211EEE0023	MASROOR AHMED	SoE	0	Ab	No	Not Eligible for Certificate
16	20211EEE0024	ANIRUDH S	SoE	80	84	Yes	





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17	20211EEE0025	RATHISH HOMBALE N	SoE	80	86	Yes	
18	20211EEE0027	YASHWANTH KUMAR S	SoE	73.33	82	Yes	
19	20211EEE0029	CETHAN S KATTI	SoE	80	82	Yes	
20	20211EEE0033	MOHAMMAD NABEEL ABBAS	SoE	73.33	88	Yes	
21	20211EEE0034	RAJANEESH B S	SoE	80	52	Yes	
22	20211EEE0035	V RAHUL BALAJIGA	SoE	80	96	Yes	
23	20211EEE0036	DEEPAK DANIEL F	SoE	80	74	Yes	
24	20211EEE0038	HEMANT PANDIT	SoE	93.33	74	Yes	
25	20211EEE0039	AKASH K	SoE	26.67	8	No	Not Eligible for Certificate
26	20211EEE0041	NAYANI POORNACHANDAN ROYAL	SoE	80	50	Yes	
27	20211EEE0044	MOHAMMED ABRAR	SoE	80	58	Yes	
28	20211EEE0046	BASIL BINU	SoE	13.33	Ab	No	Not Eligible for Certificate
29	20211EEE0050	BUDURI YASWANTH	SoE	86.67	62	Yes	
30	20211LEE0006	PRABHAS M	SoE	0	Ab	No	Not Eligible for Certificate
31	20211LEE0015	NAGENDRA B	SoE	13.33	Ab	No	Not Eligible for Certificate
32	20211LEE0019	G TARUN	SoE	0	Ab	No	Not Eligible for Certificate

**Name of Course**

Mr. Ravi V Angadi

**Instructor :**

**Employee ID of Course**

PUNIV01021

**Instructor:**

Signature of Instructor in-charge



Signature of HoD

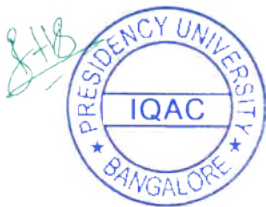
Head of the Department,  
Electrical and Electronics Engineering  
School of Engineering  
PRESIDENCY UNIVERSITY  
Kogalur, Melkote, Bangalore - 56





**School of Engineering**  
**Department of Electrical & Electronics Engineering**  
**Value Added Course offered during the Even Semester 2021-2022**

<b>Course Code:</b>	EEEV018
<b>Course Name:</b>	Introduction to MATLAB/Simulink
<b>Area of Specialization:</b>	Power Electronics, Control Systems
<b>Course Description:</b>	This course gives an introduction to MATLAB programming language and Simulink toolbox which is a graphical extension of MATLAB. This technical computing environment helps the students to involve in more intense problem solving applications and provides an opportunity to tackle realistic and more complicated problems.
<b>Course Outcome:</b>	On successful completion of the course, the student shall be able to: CO.1. Discuss the basic features of MATLAB development environment. CO.2. Summarize the MATLAB Graphical user interface. CO.3. Explain various toolboxes in engineering applications. CO.4. Construct Simulink block diagram for the given physical system.
<b>Course Content:</b>	<b>Module No 1:</b> Introduction to MATLAB, how to create variables in MATLAB, constructing M.Files, matrix operations, control flow, various arithmetic and logical operators, graphics, debugging of M.Files [8- Hours] <b>Module No 2:</b> Introduction to Simulink, various Simulink libraries, constructing Simulink block diagram [8- Hours] <b>Module No 3:</b> Toolboxes available in MATLAB: Control system toolbox, Simpower systems toolbox, Fuzzy logic toolbox, Neural network toolbox. [8- Hours] <b>Module No 4:</b> Simulation of various electrical and electronic circuits and block diagrams using Simulink [6- Hours]
<b>Instructor In-charge:</b>	Dr Jisha L K



**School of Engineering  
Department of Electrical & Electronics Engineering  
AY 2021-22 (Even Sem)**

**Value added Course(VAC) Name and Code: Introduction to MATLAB/Simulink & EEEV018**

**Name of the Instructor: Dr. Jisha L K**

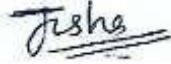
**Attendance Sheet**

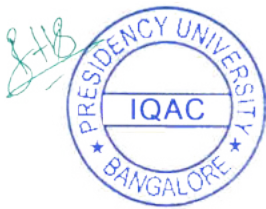
S.No.	STUDENT ID NO	STUDENT NAME	Total classes conducted	Total classes attended	Percentage attended
1	20211EEE0004	YAMUNA MN	30	22	75%
2	20211EEE0007	GAGANMURTHY	30	30	100%
3	20211eee0008	HRUTHIK HB	30	24	80%
4	20211EEE0009	ANUSHA B	30	22	75%
5	20211EEE0010	SUPRITH.D.L	30	23	78%
6	20211EEE0011	NITHISH U	30	25	82%
7	20211EEE0012	VIDYA SHREE G N	30	30	100%
8	20211EEE0013	R V GANESH	30	30	100%
9	20211EEE0017	KAVYA N	30	30	100%
10	20211EEE0019	BHARATH.H.D	30	30	100%
11	20211EEE0037	KHALEEL H TELSUNG	30	0	0%
12	20211EEE0042	ABHISHEK BASAVARAJ HAMPANNAVAR	30	0	0%
13	20211EEE0043	RISHIKA	30	22	75%
14	20211EEE0052	YENNABOINA RAHUL	30	22	75%
15	20211EEE0055	SETTIPALLI SAINATH	30	0	0%
16	20211LEE0001	DEEP CHATTERJEE	30	0	0%
17	20191EEE0003	ANUSHA M JOLAD	30	22	75%
18	20191EEE0005	ASFIYA AAZIM	30	24	80%
19	20191EEE0006	ASHISH SINGH BHUMIJ	30	30	100%
20	20191EEE0008	BINDHU D	30	30	100%
21	20191EEE0014	KRUTHIKA R	30	0	0%
22	20191EEE0016	MOHAMMAD JAMEEL	30	0	0%
23	20191EEE0017	MOHAMMAD ZAID FAROOQ	30	0	0%



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24	20191EEE0018	MOHAMMED NOORUDDIN ASRAR	30	30	100%
25	20191EEE0019	MOKA ABHINASH	30	22	75%
26	20191EEE0022	NANDA KISHORE KIRAN DESHPANDE	30	0	0%
27	20191EEE0034	S R METHESWAR	30	0	0%
28	20191EEE0036	SAMBHRAM P TAILANG	30	30	100%
29	20191EEE0053	RAHUL RAMESH PAMMAR	30	0	0%
30	20201LEE0011	GIRISH REDDY MAMILLA	30	0	0%
<b>Signature of Course Instructor</b>					



**School of Engineering**  
**Department of Electrical & Electronics Engineering**  
**Value Added Course Marksheets**

Course Code :	EEEEV018		Academic Year :			2021-22	
	Course Name :	Introduction to MATLAB/Simulink		Semester :			Even Semester
Instructor-in-Charge Name:				Dr. Jisha L K			
Instructor-in-Charge Employee ID :				PUNIV01427			
S. No	Roll No	Name	School SoE/SoL (etc)	Attendance (in %)	Marks	Eligible for Certificate (Y/N)	Remark
1	20211EEE0004	YAMUNA	SoE	75%	56	YES	
2	20211EEE0007	GAGANMURTHY	SoE	100%	70	YES	
3	20211eee0008	HRUTHIK H B	SoE	80%	64	YES	
4	20211EEE0009	Anusha B	SoE	75%	62	YES	
5	20211EEE0010	SUPRITH D L	SoE	78%	64	YES	
6	20211EEE0011	Nithish U	SoE	82%	64	YES	
7	20211EEE0012	VIDYA SHREE G N	SoE	100%	75	YES	
8	20211EEE0013	R V GANESH	SoE	100%	75	YES	
9	20211EEE0017	KAVYA N	SoE	100%	82	YES	
10	20211EEE0019	BHARATH H D	SoE	100%	82	YES	
11	20211EEE0037	Khaleel H Telsung	SoE	0%	0	NO	Not Eligible for Certificate
12	20211EEE0042	Abhishek Basavaraj Hampannavar	SoE	0%	0	NO	Not Eligible for Certificate
13	20211EEE0043	Rishika	SoE	75%	76	YES	
14	20211EEE0052	YENNABOINA RAHUL	SoE	75%	72	YES	
15	20211EEE0055	Settipalli Sainath	SoE	0%	0	NO	Not Eligible for Certificate



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(Established under the Presidency University Act, 2013 of the Karnataka Act 41 of 2013)

16	20211LEE0001	DEEP CHATTERJEE	SoE	0%	0	NO	Not Eligible for Certificate
17	20191EEE0003	ANUSHA M JOLAD	SoE	75%	54	YES	
18	20191EEE0005	ASFIYA AAZIM	SoE	80%	60	YES	
19	20191EEE0006	ASHISH SINGH BHUMIJ	SoE	100%	66	YES	
20	20191EEE0008	BINDHU D	SoE	100%	66	YES	
21	20191EEE0014	KRUTHIKA R	SoE	75%	50	YES	
22	20191EEE0016	MOHAMMAD JAMEEL	SoE	0%	0	NO	Not Eligible for Certificate
23	20191EEE0017	MOHAMMAD ZAID FAROOQ	SoE	0%	0	NO	Not Eligible for Certificate
24	20191EEE0018	MOHAMMED NOORUDDIN ASRAR	SoE	100%	64	YES	
25	20191EEE0019	MOKA ABHINASH	SoE	75%	50	YES	
26	20191EEE0022	NANDA KISHORE KIRAN DESHPANDE	SoE	0%	0	NO	Not Eligible for Certificate
27	20191EEE0034	S R METHESWAR	SoE	0%	0	NO	Not Eligible for Certificate
28	20191EEE0036	SAMBHRAM P TAILANG	SoE	100%	54	YES	
29	20191EEE0053	RAHUL RAMESH PAMMAR	SoE	0%	0	NO	Not Eligible for Certificate
30	20201LEE0011	GIRISH REDDY MAMILLA	SoE	0%	0	NO	Not Eligible for Certificate

Name of Course                      Dr. Jisha L K  
Instructor :  
Employee ID of                         PUNIV01427  
Course Instructor:

Signature of  
Instructor in charge



Signature of HoD

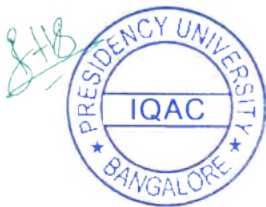
Head of the Department,  
Electrical and Electronics Engineering  
School of Engineering  
PRESIDENCY UNIVERSITY  
KARNATAKA  
REGISTRAR





**School of Engineering**  
**Department of Electrical & Electronics Engineering**  
**Value Added Course offered during the Even Semester 2021-2022**

<b>Course Code:</b>	EEEEV011
<b>Course Name:</b>	Fundamentals of Scilab programming
<b>Area of Specialization:</b>	Electrical Engineering
<b>Course Description:</b>	This course will introduce the basic concepts of scientific programming and simulations using Scilab. It will enable to perform numerical computations and analysis in all major scientific areas in all branches of engineering. This course requires the fundamentals of basic mathematics. This course is designed for beginners and at the end the students will get the ability to perform scientific computations and Simulink model development using XCos environment...
<b>Course Outcome:</b>	On successful completion of the course, the student shall be able to: CO.1. Describe the SCILAB software environment. CO.2. Discuss the concepts of basic SCILAB programming for engineering applications CO.3. Demonstrate the implementation of Xcos Simulink Environment CO.4. Interpret data from datasheet and to perform statistical analysis.
<b>Course Content:</b>	<b>Module No 1:</b> Introduction to Scilab environment- Scilab datatypes, variables and constants , Functions in Scilab Changing axes properties in scilab plots Plotting Bar graphs in Scilab [5- Hours] <b>Module No 2:</b> Entering Matrices and basic matrix operations of addition and multiplication transpose. Generating Matrices, the load Function, M-Files, Deleting Rows and Columns, Solving linear algebraic equations in Scilab. [8- Hours] <b>Module No 3:</b> Statistical computations, statistical functions- sum, mean value, median, Standard deviation. [9- Hours] <b>Module No 4:</b> Scilab toolboxes for the analyzing the systems, Solving Differential Equations in Xcos, Transfer function approach to solve ODEs. [8- Hours]
<b>Instructor In-charge:</b>	Dr. V Joshi Manohar





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## School of Engineering Department of Electrical & Electronics Engineering

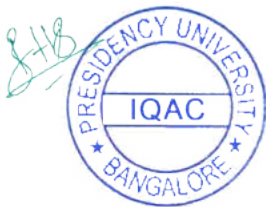
AY 2021-22 (Even Sem)

Value added Course(VAC) Name and Code: Fundamentals of Scilab programming & EEEV011

Name of the Instructor: Dr. V Joshi Manohar

### Attendance Sheet

S.No.	STUDENT ID NO	STUDENT NAME	Total classes conducted	Total classes attended	Percentage attended
1	20191EEE0004	ARUN S	30	3	10.00
2	20191EEE0013	KOTHAKOTA JAIRAM KRISHNA	30	22	73.33
3	20191EEE0015	MANDADI KARTHIKEYAN REDDY	30	26	86.67
4	20191EEE0023	NAVYA N	30	24	80.00
5	20191EEE0026	BHARGAV	30	20	76.67
6	20191EEE0031	PRUDHVI D KUDACHI	30	21	70.00
7	20191EEE0044	SHWETHA N	30	24	80.00
8	20191EEE0045	SIVA PRASAD L	30	22	73.33
9	20191eee0046	SOURODIPTTO MONDAL	30	7	23.33
10	20201EAE0003	ANAND UR	30	3	10.00
11	20201EEE0001	SOONU KUMAR	30	2	6.67
12	20201EEE0015	ABHISHEK TT	30	12	40.00
13	20211EAE0027	DUSHANTH B	30	6	20.00
14	20211EEE0005	HARIKRISHNA	30	8	26.67
15	20211EEE0016	GAGAN SAI A S	30	10	33.33
16	20211LEE0002	THATHIREDDY PERUMAL	30	2	6.67
17	20211IEE0003	FAKIR SAHID SALIMSHA	30	4	13.33
Signature of Course Instructor					





**School of Engineering**  
**Department of Electrical & Electronics Engineering**  
**Value Added Course Marksheet**

Course Code :	EEEV011		Academic Year :			2021-22	
Course Name :	Fundamentals of Scilab programming		Semester :			Even Semester	
			Instructor-in-Charge Name:			Dr. V Joshi Manohar	
			Instructor-in-Charge Employee ID :			PUNIV01153	
S. No	Roll No	Name	School (e.g. SoE/SoL etc)	Attendance (in %)	Marks	Eligible for Certificate (Y/N)	Remark
1	20191EEE0004	ARUN S	SOE	10.00	AB	NO	Not Eligible for Certificate
2	20191EEE0013	KOTHAKOTA JAIRAM KRISHNA	SOE	73.33	85	YES	
3	20191EEE0015	MANDADI KARTHIKEYAN REDDY	SOE	86.67	92	YES	
4	20191EEE0023	NAVYA N	SOE	80.00	85	YES	
5	20191EEE0026	BHARGAV	SOE	76.67	50	YES	
6	20191EEE0031	PRUDHVI D KUDACHI	SOE	70.00	75	YES	
7	20191EEE0044	SHWETHA N	SOE	80.00	85	YES	
8	20191EEE0045	SIVA PRASAD L	SOE	73.33	80	YES	
9	20191eee0046	SOURODIPTTO MONDAL	SOE	23.33	AB	NO	Not Eligible for Certificate
10	20201EAE0003	ANAND UR	SOE	10.00	AB	NO	Not Eligible for Certificate
11	20201EEE0001	SOONU KUMAR	SOE	6.67	AB	NO	Not Eligible for Certificate
12	20201EEE0015	ABHISHEK TT	SOE	40.00	AB	NO	Not Eligible for Certificate
13	20211EAE0027	DUSHANTH B	SOE	20.00	AB	NO	Not Eligible for Certificate
14	20211EEE0005	HARIKRISHNA	SOE	26.67	AB	NO	Not Eligible for Certificate
15	20211EEE0016	GAGAN SAI A S	SOE	33.33	AB	NO	Not Eligible for Certificate



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16	20211LEE0002	THATHIREDDY PERUMAL	SOE	6.67	AB	NO	Not Eligible for Certificate
17	20211IEE0003	FAKIR SAHID SALIMSHA	SOE	13.33	AB	NO	Not Eligible for Certificate

**Name of Course** Dr. V Joshi Manohar

**Instructor :**

**Employee ID of Course**

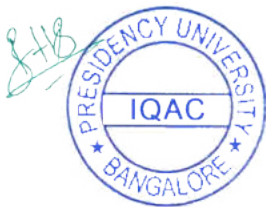
**Instructor:**

PUNIV01211

**Signature of Instructor-in-charge**

**Signature of HoD**

Head of the Department  
Electrical and Electronics Engineering  
School of Engineering  
PRESIDENCY UNIVERSITY  
Rajendranagar, Yalahanka, Bengaluru - 56





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(Established under the Presidency University Act, 2013 of the Karnataka Act 41 of 2013)

**School of Engineering**  
**Department of Electrical & Electronics Engineering**

**Value Added Course offered during the Even Semester AY 2021-22**

<b>Course Code:</b>	EEEV012
<b>Course Name:</b>	Synchronized Phasor measurement in Grid using PMUs
<b>Area of Specialization:</b>	Power System
<b>Course Description:</b>	This Course introduces to Phasor measurement unit (PMU) technology used for wide area grid monitoring to avoid blackout conditions. Advanced DSP algorithms are used to estimate the phasor value of voltage and current signals which helps in monitoring the dynamic nature of the power system. It develops analytical abilities in students with the help of Lab-VIEW Software.
<b>Course Outcome:</b>	On successful completion of the course, the student shall be able to: <b>01</b> Summarize the Power system Contingencies <b>02</b> Analyze phasor estimate for voltage and current for micro grid and conventional grid. <b>03</b> Explain the algorithm of recursive and non-recursive DFT <b>04</b> Compute and minimize the cost of reactive power consumption.
<b>Course Content:</b>	Module 1: Introduction to fourier transform, phasor estimation using discrete fourier transform method. [10 Hours]  Module 2: phasor estimation using non-recursive and recursive discrete fourier transform method in micro-grid and conventional grid, lab-view model and results using non-recursive DFT algorithm, lab-view model and results using recursive DFT algorithm [10 Hours]  Module 3: introduction to phasor measurement unit, hardware setup of phasor measurement unit and results, hardware setup using NI my-RIO, cost calculation on the basis of reactive energy consumption [10 Hours]
<b>Instructor In-charge:</b>	Mr Bishakh Paul





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**School of Engineering**

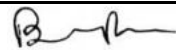


**Department of Electrical & Electronics Engineering**

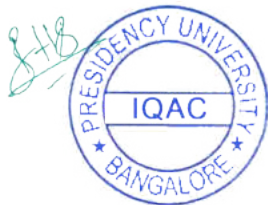
**AY 2021-22 (Even Sem)**

**Value added Course(VAC) Name and Code: Synchronized Phasor measurement in Grid using PMUs & EEEV012**

**Name of the Instructor: Mr Bishakh Paul**

**Attendance Sheet**

S.No.	STUDENT ID NO	STUDENT NAME	Total classes conducted	Total classes attended	Percentage attended
1	20191EEE0050	YARRABALLI NAVEEN	30	24	80.00
2	20191EEE0051	YASHASH N	30	23	76.67
Signature of Course Instructor					






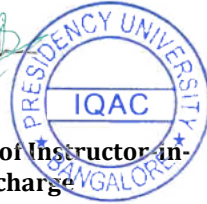
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**School of Engineering**  
**Department of Electrical & Electronics Engineering**  
**Value Added Course Marksheets**

<b>Course Code :</b>	EEEEV012		<b>Academic Year :</b>			2021-22	
<b>Course Name :</b>	Synchronized Phasor measurement in Grid using PMUs		<b>Semester :</b>			Even Semester	
			<b>Instructor-in-Charge Name:</b>			Mr Bishakh Paul	
			<b>Instructor-in-Charge Employee ID :</b>			PUNIV00895	
<b>S. No</b>	<b>Roll No</b>	<b>Name</b>	<b>School (e.g. SoE/SoL etc)</b>	<b>Attendance (in %)</b>	<b>Marks</b>	<b>Eligible for Certificate (Y/N)</b>	<b>Remark</b>
1	20191EEE0050	YARRABALLI NAVEEN	SoE	85%	80	YES	
2	20191EEE0051	YASHASH N	SoE	96%	90	YES	

**Name of Course Instructor :** Mr Bishakh Paul  
**Employee ID of Course Instructor:** PUNIV00895

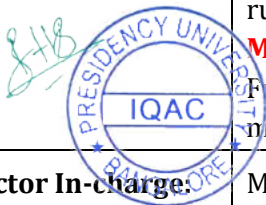
  
**Signature of Instructor in-charge**  


  
**Signature of HoD**  
Head of the Department  
Electrical and Electronics Engineering  
School of Engineering  
PRESIDENCY UNIVERSITY  
Kajanjurki, Yalahanka, Bengaluru - 56

  
**REGISTRAR**  


**School of Engineering**  
**Department of Electrical & Electronics Engineering**  
**Value Added Course offered during the Even Semester 2021-2022**

<b>Course Code:</b>	EEEV017
<b>Course Name:</b>	Design of Fuzzy Logic Systems
<b>Area of Specialization:</b>	Soft Computing Techniques
<b>Course Description:</b>	In this course, an intelligent technique for data processing drawn from complex and imprecise environment are presented and studied. Fuzzy Logic theory is based on the empirical aspect of the human reasoning, and is used in the manipulation of imperfect, imprecise or approximate knowledge. It allows the modeling and processing of very complex systems in which, for example, human factors are present. Theory and applications concerning fuzzy logic exist for more than fifty years. It covers several fields such as artificial intelligence, identification and control of dynamic systems, automatic decision-making in complex systems, and fault diagnosis in industrial processes.
<b>Course Outcome:</b>	On successful completion of the course, the student shall be able to:  CO1: Define fuzzy logic theory and membership functions CO2: Discuss operations and properties of fuzzy systems CO3: Describe fuzzy relations and fuzzy rules CO4: Describe fuzzy logic control operations and applications
<b>Course Content:</b>	<b>Module: 1: Introduction to fuzzy set</b> [12 Hrs] [Knowledge Level] Introduction of Fuzzy Logic techniques to manipulate imprecise and approximated data and systems - Fuzzy sets theory and concepts, membership functions, operations on fuzzy sets, triangular norms <b>Module: 2: Fuzzy operations</b> [13 Hrs] [Application Level] Fuzzy relations and fuzzy quantities, fuzzy intervals, fuzzy numbers, operation on fuzzy quantities, Linguistic variables, linguistic modifiers, fuzzy rules, fuzzy quantifiers <b>Module: 3: Fuzzy reasoning</b> [15 Hrs] [Application Level] Fuzzy reasoning, fuzzy implications, Fuzzy control, Mamdani and Larsen methods. Applications - MATLAB simulations using the Fuzzy Toolbox.
<b>Instructor In-charge:</b>	Mr. Sarin MV





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(Established under the Presidency University Act, 2013 of the Karnataka Act 41 of 2013)

## School of Engineering Department of Electrical & Electronics Engineering

AY 2021-22 (Even Sem)

Value added Course(VAC) Name and Code: Design of Fuzzy Logic Systems & EEEV017

Name of the Instructor: Mr. Sarin M V

### Attendance Sheet

S.No.	STUDENT ID NO	STUDENT NAME	Total classes conducted	Total classes attended	Percentage attended
1	20191EEE0001	ABHISHEK C	30	27	90
2	20191EEE0009	DOKLA GHOUSE	30	15	50
3	20191EEE0010	EASHWAR V	30	17	57
4	20191EEE0011	KEERTHANA B R	30	27	90
5	20191EEE0012	KOMALA M E	30	26	87
6	20191EEE0016	MOHAMMAD JAMEEL	30	12	40
7	20191EEE0024	NAVYA SHREE M	30	23	77
8	20191EEE0025	P ABHINAV	30	22	73
9	20191EEE0028	PRAJWAL HOSAMANI	30	23	77
10	20191EEE0029	PRAJWAL T R	30	23	77
11	20191EEE0030	PRATHVIRAJ	30	24	80
12	20191EEE0037	SANJAY B	30	27	90
13	20191EEE0038	SANJAY P	30	23	77
14	20191EEE0039	SANKET VIJAY KUMAR KAMB	30	23	77
15	20191EEE0040	SAPNA N	30	25	83
16	20191EEE0052	YASHWANTH N	30	23	77
17	20191EEE0057	ZAID AHMED ZAUED HAMADAH	30	28	93
18	20191EEE0059	Shabbirahmadmujavar	30	23	77
19	20191EEE0061	PRANEETH MADHAVAN	30	23	77
20	20201EAE0002	RAHEL ANN JOHNSON	30	24	80
21	20201EEE0005	Rakshitha.B	30	24	80
22	20201EEE0008	VARSHITHA M	30	24	80
23	20201EEE0011	SAI NAYANA	30	24	80
24	20201EEE0018	FIZA	30	24	80



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25	20201EEE0022	YASHASWINI BG	30	24	80
26	20201EEE0023	SHRUJAN H S	30	3	10
27	20201EEE0025	VISHALA R	30	6	20
28	20201LEE0005	SUHEBAHAMED BALAGANUR	30	24	80
29	20211EEE0015	Bindhu R C	30	0	0
30	20211EEE0020	Rudragouda k police patil	30	5	17
31	20211EEE0040	Mohamed Thabish	30	10	33
32	20211EEE0045	METTANI NAGA CHETHAN	30	0	0
33	20211EEE0048	SAGAR D M	30	0	0
34	20201LEE0010	MOHAMMED JAVED	30	23	77
35	20211EEE0003	SUMAN	30	26	87
36	20211EEE0014	SINCHANA M	30	25	83
37	20211EEE0022	ARINDAM DUTTA	30	25	83
38	20211EEE0026	MOHAMMED AIMAN KHAN	30	23	77
39	20211EEE0028	ADARSH A	30	27	90
40	20211EEE0053	KARRI GOWRI ESWAR	30	0	0
41	20211EPE0002	SIRICHAPALA UDAY MALIK	30	17	57
42	20201EEE0007	S THYAGARAJ	30	17	57
43	20211EEE0030	JATIN SHARMA	30	26	87
44	20211EEE0031	TEJASHWINI ANNAPPAGOUDA	30	23	77
45	20211EEE0032	MANTHU NANDHINI	30	24	80
46	20211EEE0037	KHALEEL H TELSUNG	30	23	77
47	20211EEE0047	G KIRAN KUMAR	30	23	77
48	20211EEE0051	Madivada Hemanth	30	23	77
49	20211EEE0054	SUDULAKUNTLA CHARAN	30	24	80
50	20211LEE0004	YOGENDRA	30	19	63
51	20211LEE0005	SANTHOSH V	30	19	63
52	20211LEE0007	SANJAY M K	30	20	67
53	20211LEE0008	MANOJ K P	30	15	50
54	20211LEE0009	PAVAN V	30	23	77
55	20211LEE0010	ROHIT GURUNATH MATHAPAT	30	28	93





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56	20211LEE0011	KISHORE TEJA S N	30	26	87
57	20211LEE0012	HAMSA SHREE R	30	23	77
58	20211LEE0013	CHARANREDDY S V	30	25	83
59	20201EEE0016	KAMPA PREETHISH	30	0	0
60	20211LEE0025	DARSHAN T C	30	0	0
61	20211LEE0027	KUSHAL R	30	0	0
62	20191EEE0047	SRINIDHI R	30	0	0
63	20211LEE0014	AMBIKA M BIJAPUR	30	24	80
64	20211LEE0016	NIRANJAN JAGADISH PAMMA	30	24	80
65	20211LEE0017	NARESH R N	30	23	77
66	20211LEE0018	MURULI A V	30	16	53
67	20211LEE0020	SACHIN	30	23	77
68	20211LEE0021	CHARAN	30	28	93
69	20211LEE0022	MOHAMMED SHAH ALAM	30	24	80
70	20211LEE0023	PATEL CHIKKALINGE GOWDA	30	25	83
71	20211LEE0024	MAHESH	30	24	80
72	20211LEE0026	ARUNA	30	24	80
73	20211LEE0028	SHASHANK	30	26	87
74	20211LEE0029	ABHI	30	25	83
75	20211LEE0030	BABITHA	30	25	83
76	20191EEE9006	MOHAMMED ZUHAIB	30	24	80
77	20191EEE0060	Naveen Nelson W	30	19	63
78	20211LEE0031	RAMEGOWDA KT	30	24	80
79	20211EEE0056	SHREYAS E	30	23	77
80	20201LEE0006	Vinuth Gowda R	30	27	90
81	20191EEE9002	KIRAN MANOJ	30	0	0
82	20191EEE9005	BARU V S TRIPURA MADHU DHEERAJ	30	0	0
83	20201LEE0002	SUBHAJIT BISWAS	30	0	0
84	20201LEE0004	PRAVEEN M	30	0	0
85	20201LEE0007	ASHWIN S	30	0	0
<b>Signature of Course Instructor</b>					

**School of Engineering**  
**Department of Electrical & Electronics Engineering**  
**Value Added Course Marksheet**

Course Code :	EEEEV017		Academic Year :			2021-22	
Course Name :	Design of Fuzzy Logic Systems		Semester :			Even Semester	
			Instructor-in-Charge Name:			Mr. Sarin M V	
			Instructor-in-Charge Employee ID :			PUNIV01347	
S. No	Roll No	Name	School (e.g. SoE/SoL etc)	Attendance (in %)	Marks	Eligible for Certificate (Y/N)	Remarks
1	20191EEE0001	ABHISHEK C	SoE	90	78	YES	
2	20191EEE0009	DOKLA GHOUSE	SoE	50	0	NO	Not Eligible for Certificate
3	20191EEE0010	EASHWAR V	SoE	57	0	NO	Not Eligible for Certificate
4	20191EEE0011	KEERTHANA B R	SoE	90	75	YES	
5	20191EEE0012	KOMALA M E	SoE	87	63	YES	
6	20191EEE0016	MOHAMMAD JAMEEL	SoE	40	0	NO	Not Eligible for Certificate
7	20191EEE0024	NAVYA SHREE M	SoE	77	77	YES	
8	20191EEE0025	P ABHINAV	SoE	73	73	YES	
9	20191EEE0028	PRAJWAL HOSAMANI	SoE	77	72	YES	
10	20191EEE0029	PRAJWAL T R	SoE	77	74	YES	
11	20191EEE0030	PRATHVIRAJ	SoE	80	78	YES	
12	20191EEE0037	SANJAY B	SoE	90	64	YES	
13	20191EEE0038	SANJAY P	SoE	77	65	YES	
14	20191EEE0039	SANKET VIJAY KUMAR KAMB	SoE	77	66	YES	
15	20191EEE0040	SAPNA N	SoE	83	68	YES	
16	20191EEE0052	YASHWANTH N	SoE	77	69	YES	
17	20191EEE0057	ZAID AHMED ZAUED HAMADAH	SoE	93	73	YES	
18	20191EEE0059	Shabbirahmadmujavar	SoE	77	73	YES	



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19	20191EEE9001	PRANEETH MADHAVAN	SoE	77	68	YES	
20	20201EAE0002	RAHEL ANN JOHNSON	SoE	80	64	YES	
21	20201EEE0005	Rakshitha.B	SoE	80	66	YES	
22	20201EEE0008	VARSHITHA M	SoE	80	65	YES	
23	20201EEE0011	SAI NAYANA	SoE	80	70	YES	
24	20201EEE0018	FIZA	SoE	80	71	YES	
25	20201EEE0022	YASHASWINI BG	SoE	80	73	YES	
26	20201EEE0023	SHRUJAN H S	SoE	10	56	NO	Not Eligible for Certificate
27	20201EEE0025	VISHALA R	SoE	20	69	NO	Not Eligible for Certificate
28	20201LEE0005	SUHEBAHAMED BALAGANUR	SoE	80	63	YES	
29	20211EEE0015	Bindhu R C	SoE	0	0	NO	Not Eligible for Certificate
30	20211EEE0020	Rudragouda k police patil	SoE	17	0	NO	Not Eligible for Certificate
31	20211EEE0040	Mohamed Thabish	SoE	33	0	NO	Not Eligible for Certificate
32	20211EEE0045	METTANI NAGA CHETHAN	SoE	0	0	NO	Not Eligible for Certificate
33	20211EEE0048	SAGAR D M	SoE	0	0	NO	Not Eligible for Certificate
34	20201LEE0010	MOHAMMED JAVED	SoE	77	63	YES	
35	20211EEE0003	SUMAN	SoE	87	64	YES	
36	20211EEE0014	SINCHANA M	SoE	83	68	YES	
37	20211EEE0022	ARINDAM DUTTA	SoE	83	67	YES	
38	20211EFE0026	MOHAMMED AIMAN KHAN	SoE	77	70	YES	
39	20211EEE0028	ADARSH A	SoE	90	72	YES	
40	20211EEE0053	KARRI GOWRI ESWAR	SoE	0	0	NO	Not Eligible for Certificate
41	20211EPE0002	SIRICHAPALA UDAY MALIK	SoE	57	0	NO	Not Eligible for Certificate
42	20201EEE0007	S THYAGARAJ	SoE	57	0	NO	Not Eligible for Certificate



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(Established under the Presidency University Act, 2013 of the Karnataka Act 41 of 2013)

43	20211EEE0030	JATIN SHARMA	SoE	87	71	YES	
44	20211EEE0031	TEJASHWINI ANNAPPAGOUDA	SoE	77	70	YES	
45	20211EEE0032	MANTHU NANDHINI	SoE	80	65	YES	
46	20211EEE0037	KHALEEL H TELSUNG	SoE	77	66	YES	
47	20211EEE0047	G KIRAN KUMAR	SoE	77	67	YES	
48	20211EEE0051	Madivada Hemanth	SoE	77	68	YES	
49	20211EEE0054	SUDULAKUNTLA CHARAN	SoE	80	62	YES	
50	20211LEE0004	YOGENDRA	SoE	63	0	NO	Not Eligible for Certificate
51	20211LEE0005	SANTHOSH V	SoE	63	0	NO	Not Eligible for Certificate
52	20211LEE0007	SANJAY M K	SoE	67	0	NO	Not Eligible for Certificate
53	20211LEE0008	MANOJ K P	SoE	50	0	NO	Not Eligible for Certificate
54	20211LEE0009	PAVAN V	SoE	77	69	YES	
55	20211LEE0010	ROHIT GURUNATH MATHAPAT	SoE	93	70	YES	
56	20211LEE0011	KISHORE TEJA S N	SoE	87	65	YES	
57	20211LEE0012	HAMSA SHREE R	SoE	77	66	YES	
58	20211LEE0013	CHARANREDDY S V	SoE	83	68	YES	
59	20201EEE0016	KAMPA PREETHISH	SoE	0	0	NO	Not Eligible for Certificate
60	20211LEE0025	DARSHAN T C	SoE	0	0	NO	Not Eligible for Certificate
61	20211LEE0027	KUSHAL R	SoE	0	0	NO	Not Eligible for Certificate
62	20191EEE0047	SRINIDHI R	SoE	0	0	NO	Not Eligible for Certificate
63	20211LEE0014	AMBIKA M BIJAPUR	SoE	80	63	YES	
64	20211LEE0016	NIRANJAN JAGADISH PAMMA	SoE	80	64	YES	
65	20211LEE0017	NARESH R N	SoE	77	70	YES	
66	20211LEE0018	MURULI A V	SoE	53	0	NO	Not Eligible for Certificate



# PRESIDENCY UNIVERSITY

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67	20211LEE0020	SACHIN	SoE	77	73	YES	
68	20211LEE0021	CHARAN	SoE	93	74	YES	
69	20211LEE0022	MOHAMMED SHAH ALAM	SoE	80	68	YES	
70	20211LEE0023	PATEL CHIKKALINGE GOWDA	SoE	83	66	YES	
71	20211LEE0024	MAHESH	SoE	80	68	YES	
72	20211LEE0026	ARUNA	SoE	80	63	YES	
73	20211LEE0028	SHASHANK	SoE	87	64	YES	
74	20211LEE0029	ABHI	SoE	83	67	YES	
75	20211LEE0030	BABITHA	SoE	83	70	YES	
76	20191EEE9006	MOHAMMED ZUHAIB	SoE	80	72	YES	
77	20191EEE0060	Naveen Nelson W	SoE	63	0	NO	Not Eligible for Certificate
78	20211LEE0031	RAMEGOWDA KT	SoE	80	66	YES	
79	20211EEE0056	SHREYAS E	SoE	77	68	YES	
80	20201LEE0006	Vinuth Gowda R	SoE	90	65	YES	
81	20191EEE9002	KIRAN MANOJ	SoE	0	0	NO	Not Eligible for Certificate
82	20191EEE9005	BARU V S TRIPURA MADHU DHEERAJ	SoE	0	0	NO	Not Eligible for Certificate
83	20201LEE0002	SUBHAJIT BISWAS	SoE	0	0	NO	Not Eligible for Certificate
84	20201LEE0004	PRAVEEN M	SoE	0	0	NO	Not Eligible for Certificate
85	20201LEE0007	ASHWIN S	SoE	0	0	NO	Not Eligible for Certificate

Name of Course

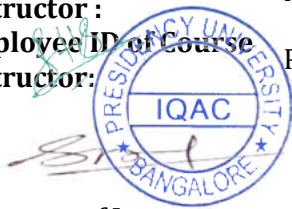
Mr. Sarin M V

Instructor :

Employee ID of Course

PUNIV01347

Instructor:



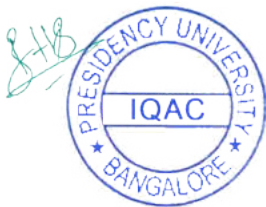
Signature of Instructor-  
in-charge

Signature of HoD  
Head of the Department  
Electrical and Electronic Engineering  
School of Engineering  
PRESIDENCY UNIVERSITY  
Rajarajeshwari, Yalahanka, Bengaluru - 56




**School of Engineering**  
**Department of Electrical & Electronics Engineering**  
**Value Added Course offered during the Even Semester 2021-2022**

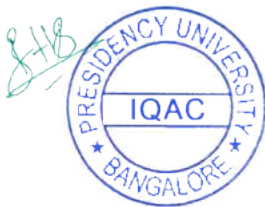
<b>Course Code:</b>	EEEV016
<b>Course Name:</b>	Design and Simulation of DC-DC converters
<b>Area of Specialization:</b>	Power Electronics
<b>Course Description:</b>	This course is design oriented. There are many dc-dc converter topologies that are used in power supplies. This course emphasizes learning and understanding the topologies through the aid of mat lab or open sources tools like octave, GEDA and spice. The course starts with a discussion on rectifier circuits and leads on up to multi-output dc-dc converters. The discussion on the various topologies is strengthened with the aid of simulation demonstrations and design exercises.
<b>Course Outcome:</b>	On successful completion of the course, the student shall be able to:CO.1. Explain the basics of rectifiers and power Semi-conductor devices CO.2. Discuss the operation of various isolated and non-isolated converter CO.3. Analyse the design fundamentals and simulation of various dc dc converter topologies.
<b>Course Content:</b>	<b>Module No 1:</b> operation and design of rectifiers, Rectifier - Capacitor filter, circuit operation and waveforms, designing the circuit, setting up for simulation in spice/mat lab, simulation of circuit [5- Hours] <b>Module No 2:</b> Ideal switch, diodes, reading the diode datasheet, thermal dissipation, heatsink design, Bipolar junction transistor, MOSFETs and IGBTs [8- Hours] <b>Module No 3:</b> Linear DC -DC converter or linear regulators, DC-DC switched mode converters: Buck, Boost and buck-boost converters, operation, waveforms, equations and simulation [9- Hours] <b>Module No 4:</b> Forward converter operation, waveforms, , Fly back converter, operation and waveforms, Push pull, half bridge and full bridge circuits, operation and waveforms, simulation example, basics of drive circuits [8- Hours]
<b>Instructor In-charge:</b>	Ms. Ragasudha C P





**School of Engineering**  
**Department of Electrical & Electronics Engineering**  
**AY 2021-22 (Even Sem)**  
**Value added Course(VAC) Name and Code: Design and Simulation of DC-DC converters & EEEV016**  
**Name of the Instructor: Ms. Ragasudha C P**  
**Attendance Sheet**

S.No.	STUDENT ID NO	STUDENT NAME	Total classes conducted	Total classes attended	Percentage attended
1	20201LEE0009	BHARATH KUMAR S	30	30	100.00
2	20191EEE0033	ROSHAN S	30	18	60.00
Signature of Course Instructor					



**School of Engineering**  
**Department of Electrical & Electronics Engineering**  
**Value Added Course Marksheet**

<b>Course Code :</b>	EEEEV016		<b>Academic Year :</b>			2021-22	
<b>Course Name :</b>	Design and Simulation of DC-DC converters		<b>Semester :</b>			Even Semester	
			<b>Instructor-in-Charge Name:</b>			Ms. Ragasudha C P	
			<b>Instructor-in-Charge Employee ID :</b>			PUNIV01324	
<b>S. No</b>	<b>Roll No</b>	<b>Name</b>	<b>School (e.g. SoE/SoL etc)</b>	<b>Attendance (in %)</b>	<b>Marks</b>	<b>Eligible for Certificate (Y/N)</b>	<b>Remark</b>
1	20201LEE0009	BHARATH KUMAR S	SoE	100%	92	Yes	
2	20191EEE0033	ROSHAN S	SoE	60%	AB	No	Not Eligible for Certificate

**Name of Course**

**Instructor :**

**Employee ID of Course**

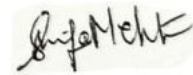
**Instructor:**

Ms. Ragasudha C P

PUNIV01324



**Signature of Instructor-in-charge**



**Signature of HoD**

Head of the Department,  
Electrical and Electronics Engineering  
School of Engineering  
PRESIDENCY UNIVERSITY  
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