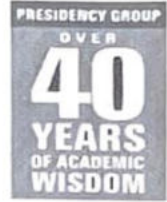




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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

CERTIFICATE

Certified that this dissertation "IMPLEMENTATION OF A REAL TIME DATA ACQUISITION SYSTEM ON FPGA" is a bonafide work of "MOHAMMED FURQHAN(20192ESV0002)", who has successfully carried out the dissertation work under my supervision.

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This is to certify that the University Project titled "CONFIGURING ETHERNET LAN8720A ON SPARTAN 6 FPGA BOARD" was carried out by STUDENT NAME Prasanta Kumar Pradhan AND ID 20192ESV0001 who is a bonafide student IV Semester M.Tech, Embedded Systems and VLSI in Presidency University. This is in partial fulfilment of the course work for Master of Technology in Presidency University, Bengaluru, during the year 2020.



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Examiner-2



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This is to certify that the Dissertation Industrial Project titled "**DATA ACQUISITION SYSTEM USING USB**" was carried out by Simran R (ID No.: 20192ESV0003) who is a bonafide student IV Semester M.Tech, Embedded Systems and VLSI in Presidency University. This is in fulfilment of the course work for Master of Technology in Presidency University, Bengaluru, during the year 2021.

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Examiner-2

